WARP

**WIRELESS OPEN-ACCESS
RESEARCH PLATFORM**

Lab 1: Introduction to WARP Design Flows

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WARP Project

1   Introduction

In this lab, you will use Xilinx Platform Studio and Base System Builder to construct a simple hardware & software platform, then test your design on the WARP FPGA Board. The instructions for this exercise are on the WARP web site.

Note: All files are stored in `C:\workshop\userN\` where `userN` is your user login location. This location will be referred to as `.\` for the rest of the lab.

2   Building Your Base System

Please visit http://warp.rice.edu/trac/wiki/Exercises/XPSIntro to complete this exercise.

The link will guide you through a step-by-step process of creating your own base system, but keep the following things in mind.

1. Use Xilinx Platform Studio 9.1i.

2. Save your project in `.\Lab1_EDK\xps\system.xmp`.

3. You'll use the remote server to implement the design and your local PC to download your FPGA bitstream and test the design.