

Networking on WARP

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Rice University

WARP Workshop
December 2, 2007

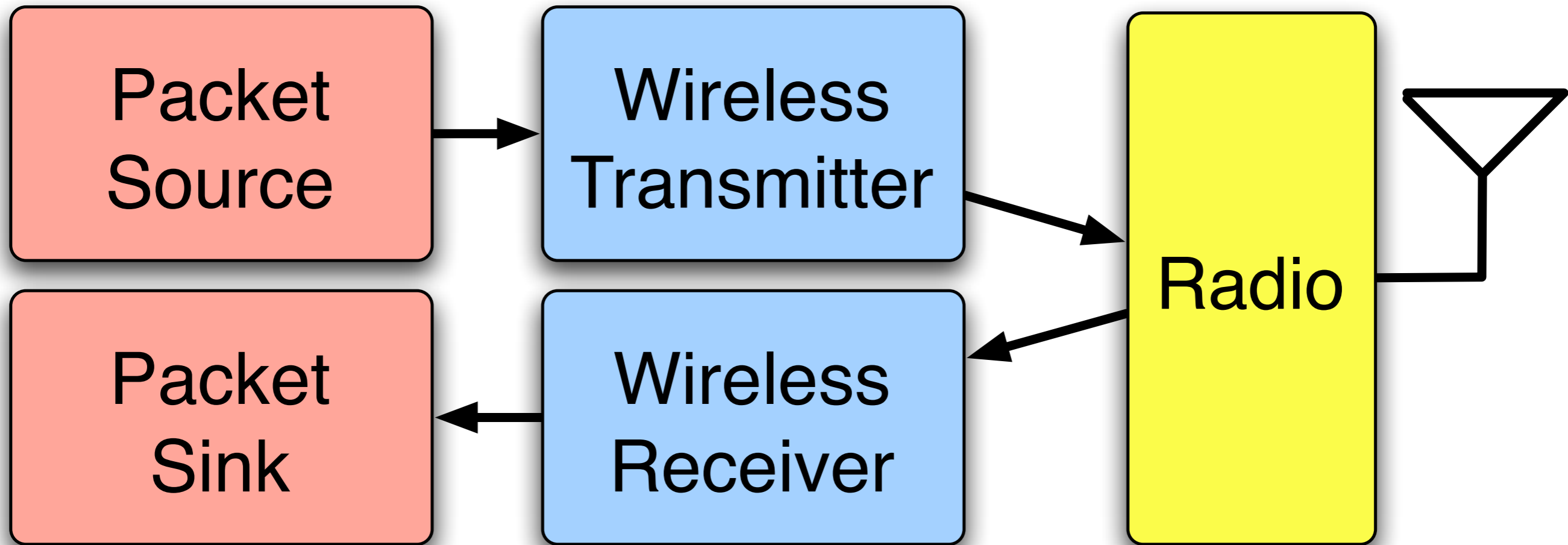


Today's Agenda

- Questions from yesterday?
- Networking on WARP lecture
- Lab 4 - Simple "MAC" layer
- Lab 5 - Unidirectional MAC
- Lab 6 - Channel-hopping MAC
- Workshop wrap-up

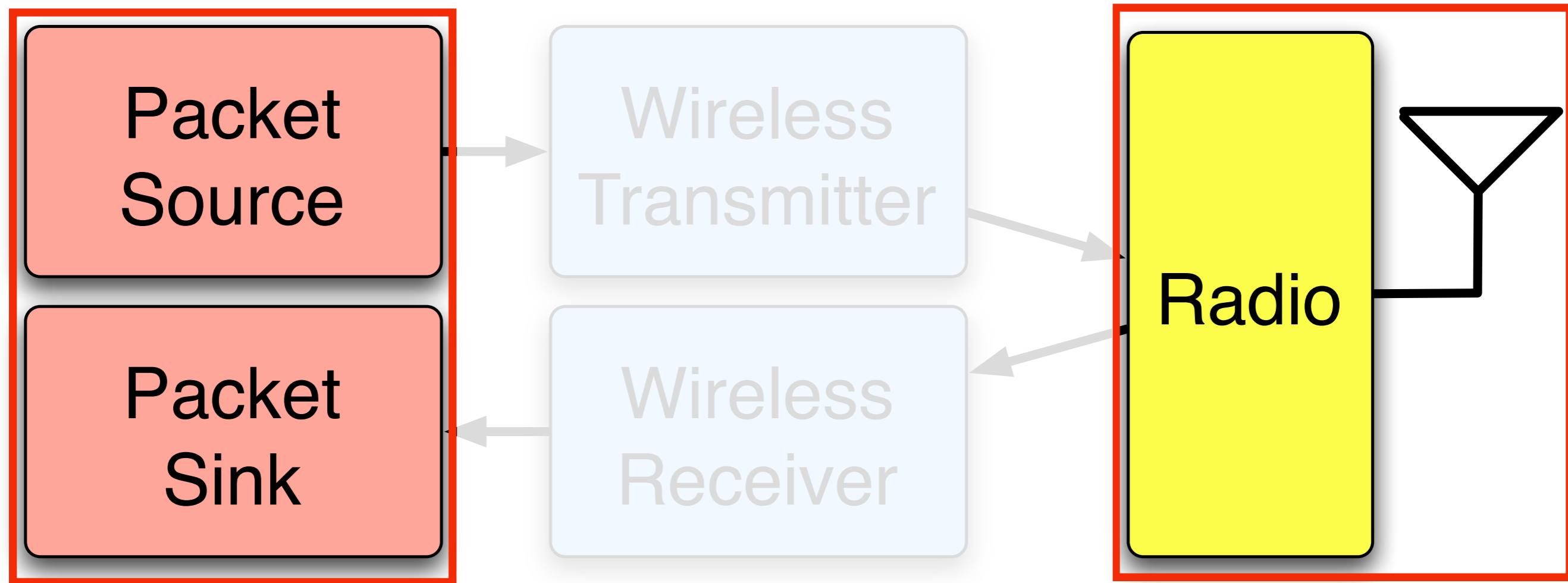
Physical Layer Basics

Simple Wireless Node



Physical Layer Basics

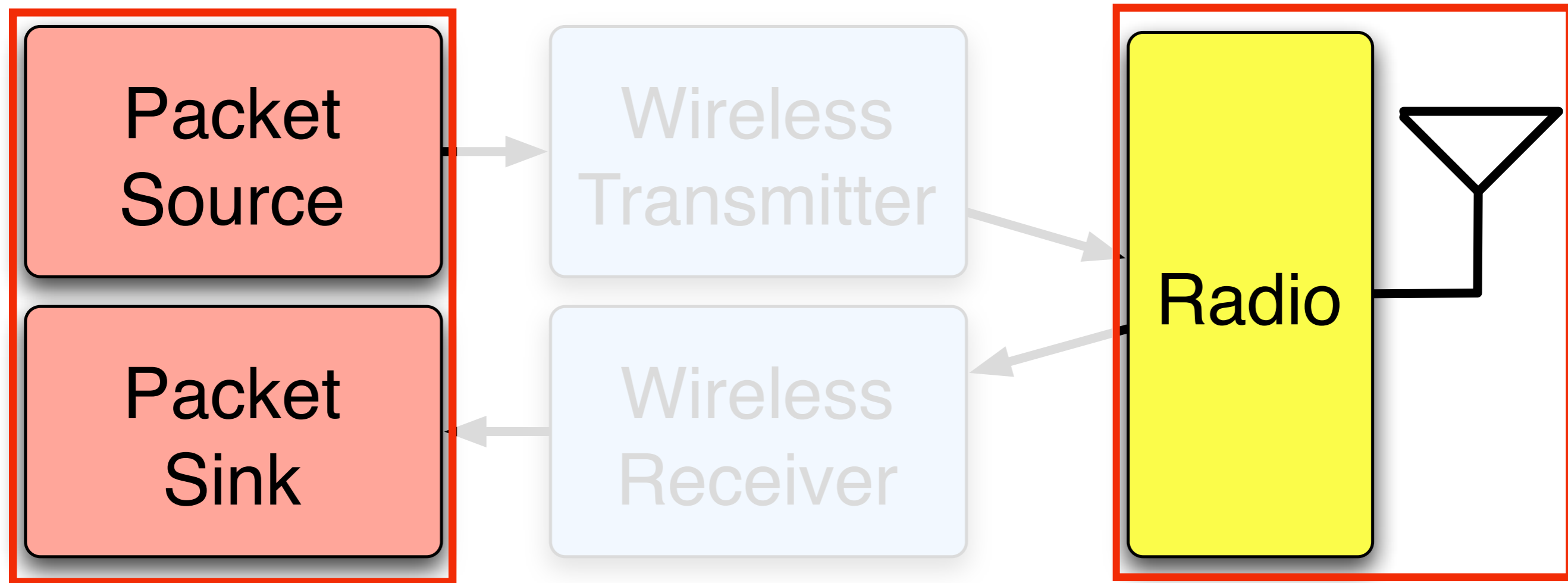
Simple Wireless Node



Somebody Else's Problem

Network Layer Basics

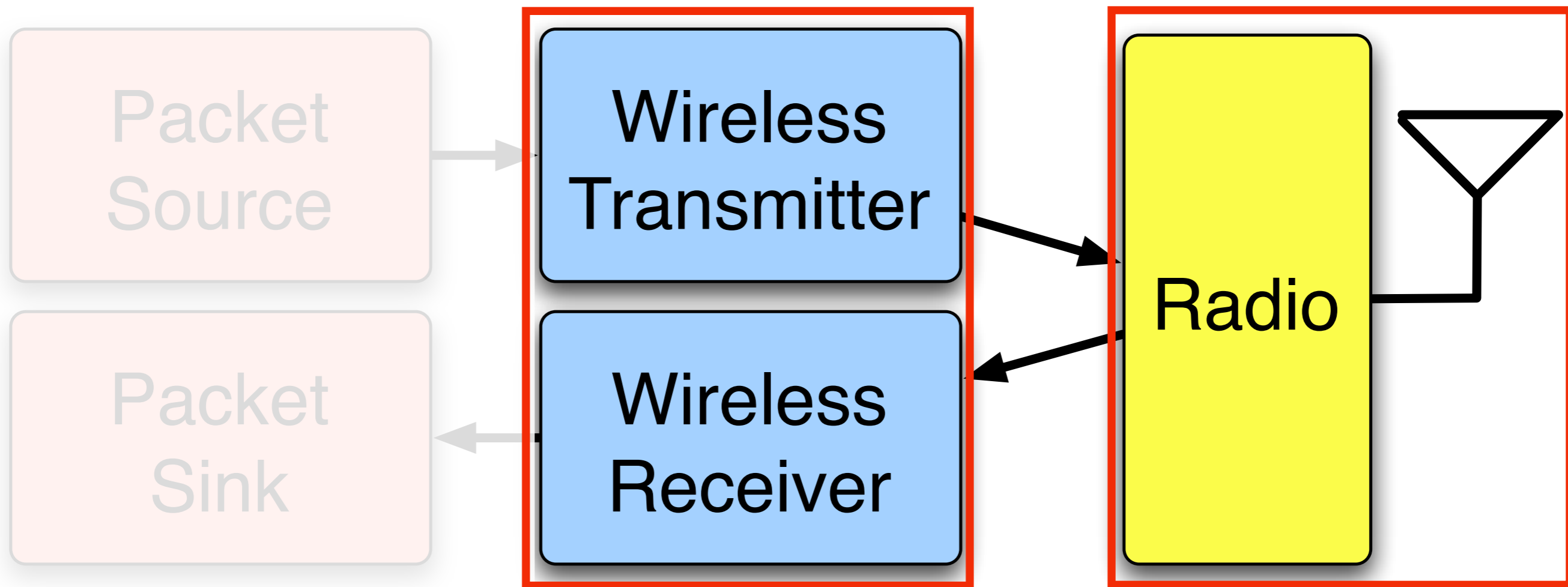
Simple Wireless Node



Somebody Else's Problem

Network Layer Basics

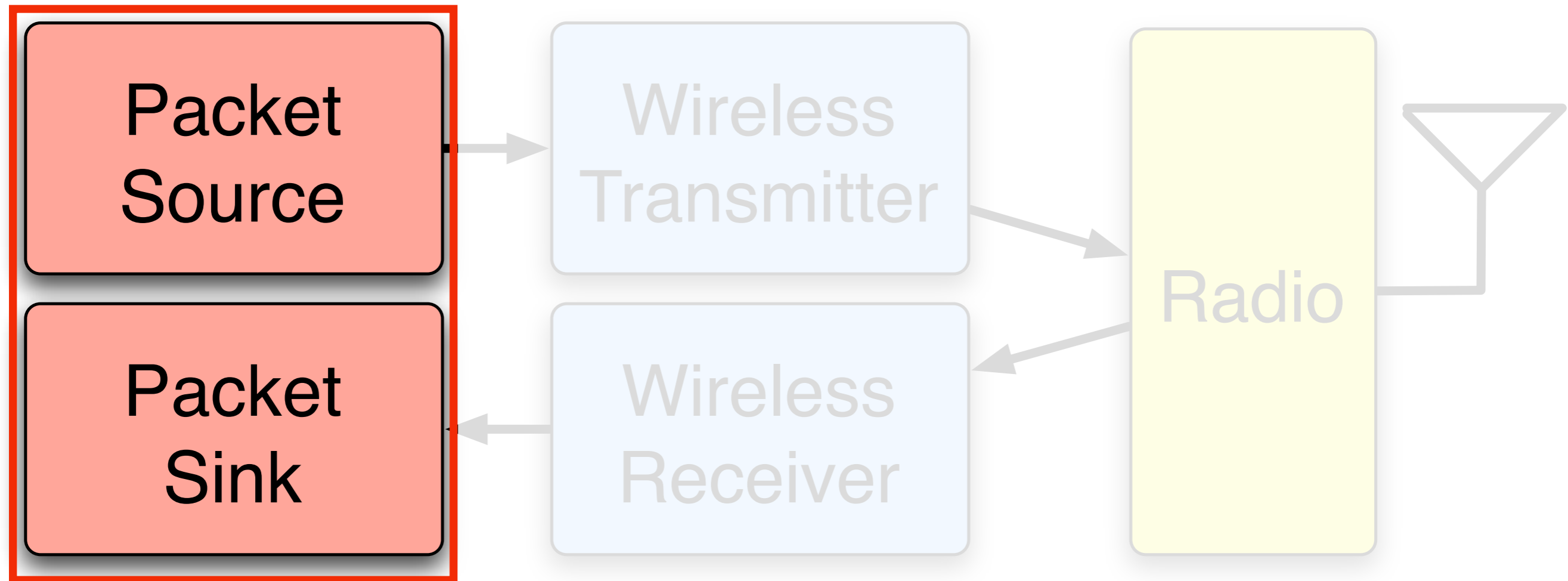
Simple Wireless Node



Somebody Else's Problem

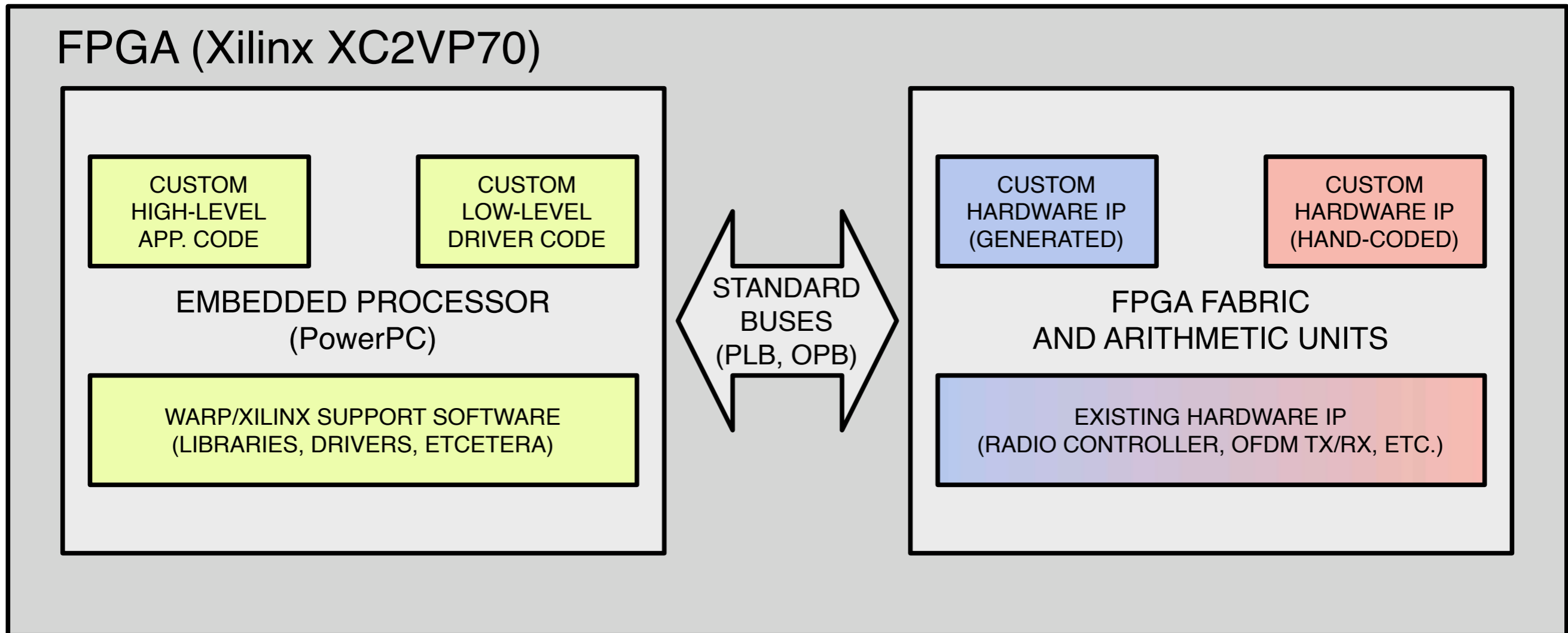
Network Layer Basics

Simple Wireless Node



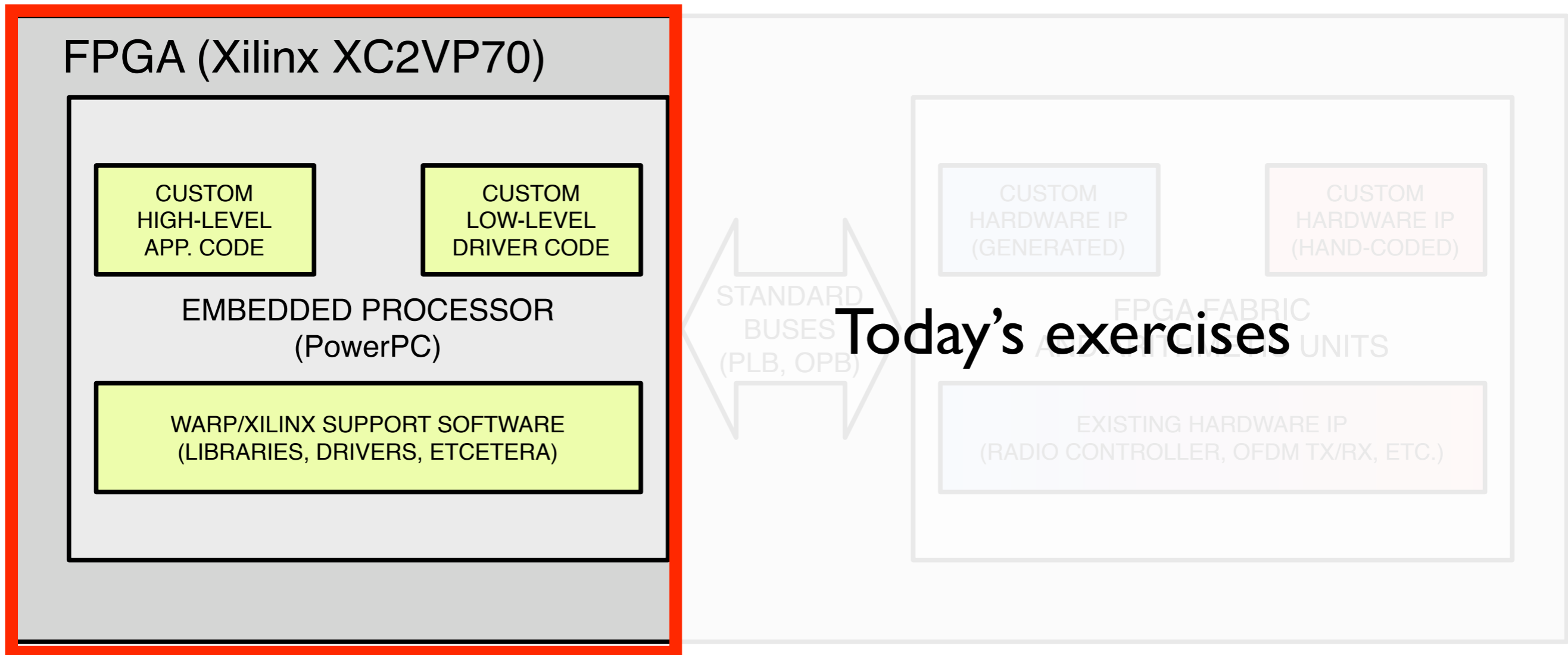
Targeting WARP Hardware

(Understanding the Development Environment)

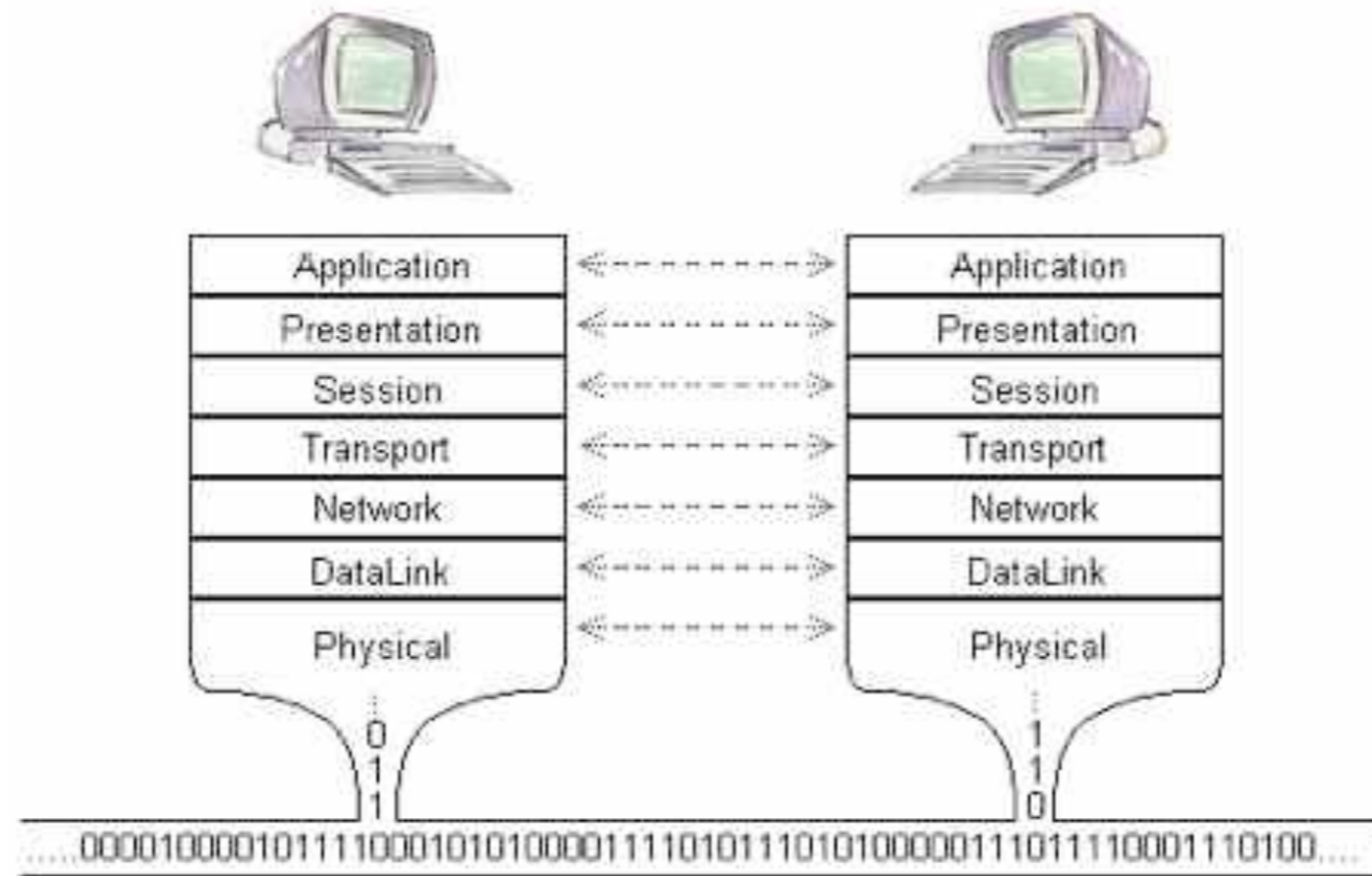


Targeting WARP Hardware

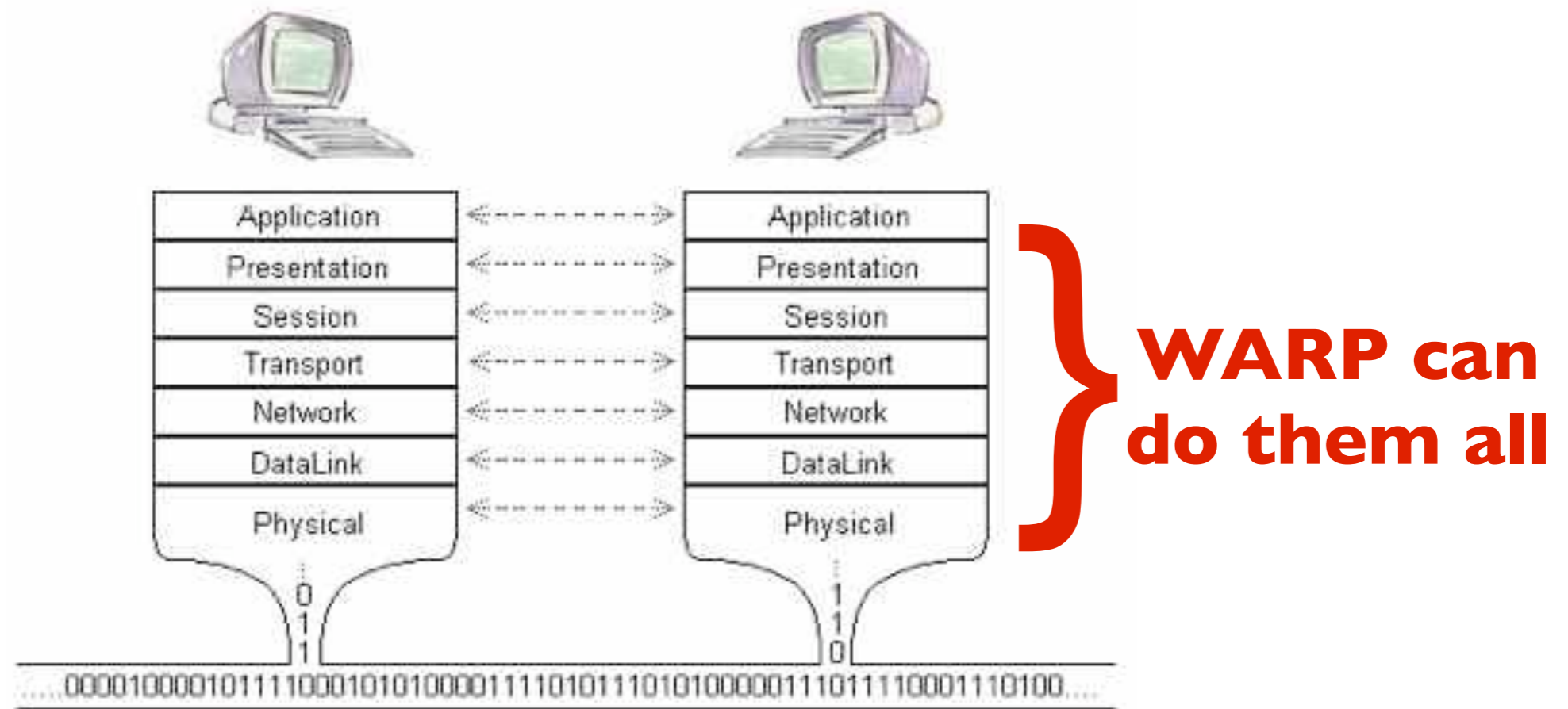
(Understanding the Development Environment)



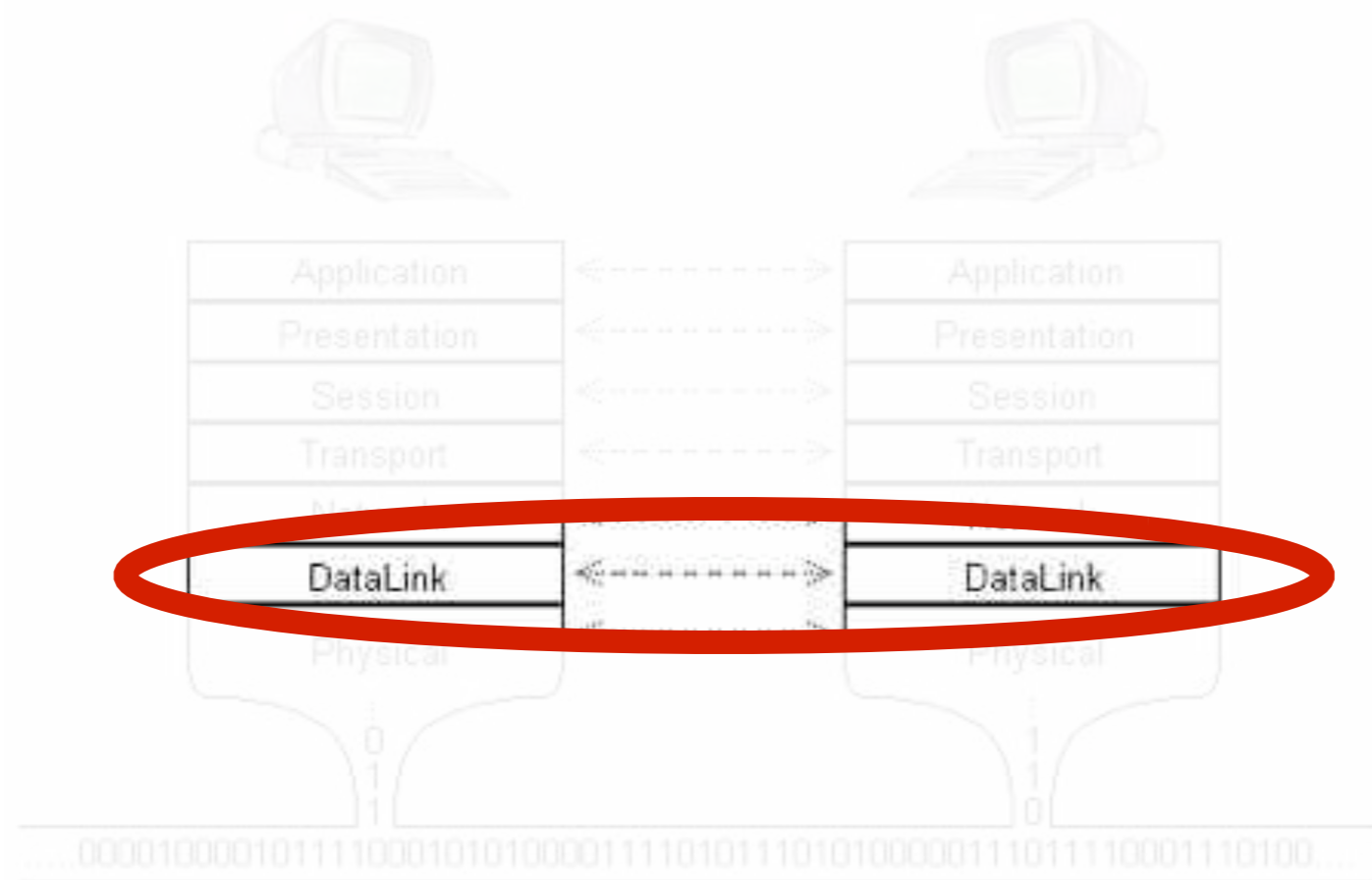
Some Perspective - The OSI Model



Some Perspective - The OSI Model

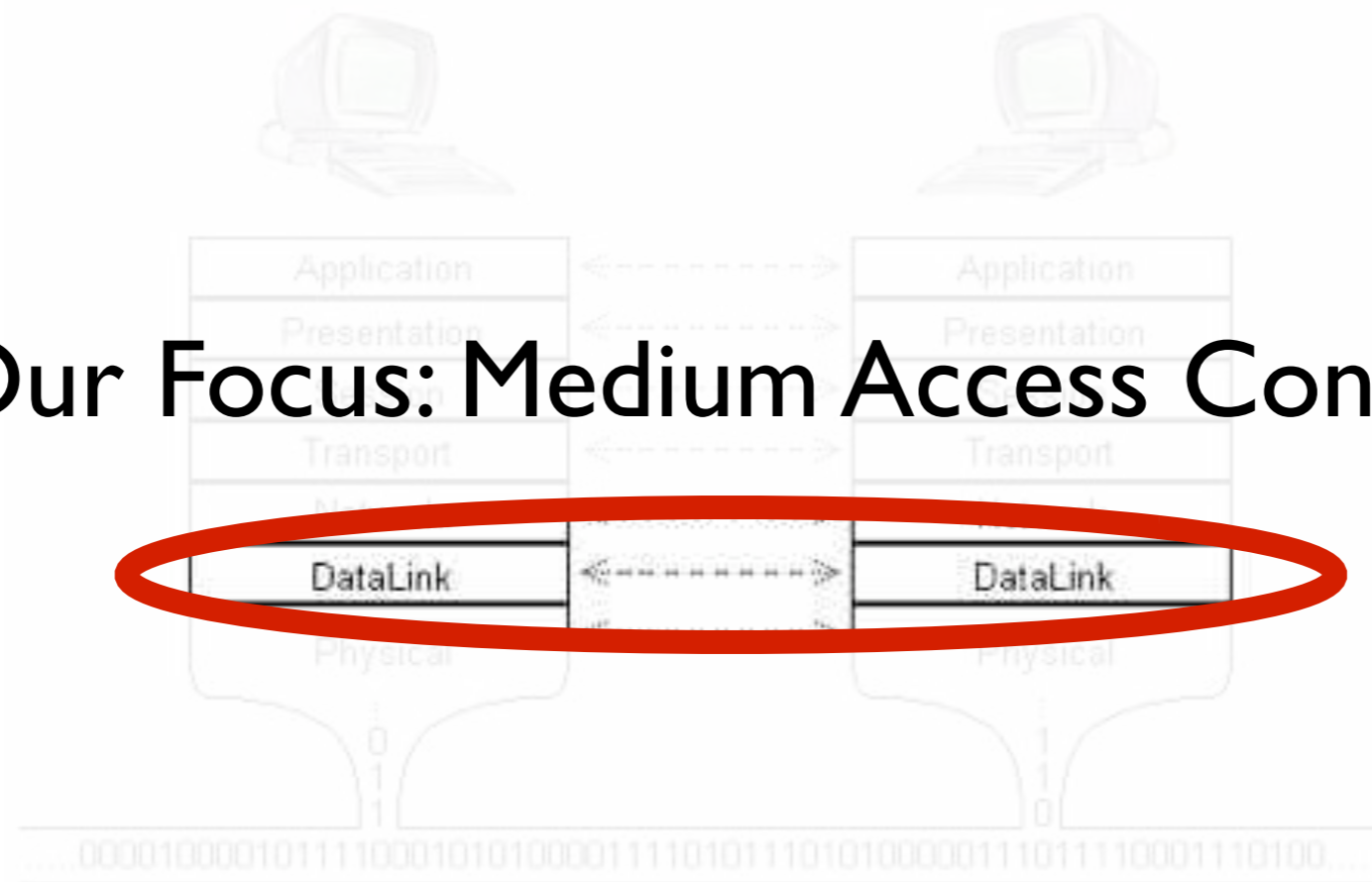


The OSI Model



The OSI Model

Our Focus: Medium Access Control



The OSI Model

- Why?
- Many interesting research problems: mesh networks, adaptive rate, cross-layer gains, etc.
- All commercial 802.11 chipsets are closed

Outline

- Overview of Medium Access Control
- Design Realization
- WARPMAC Framework
- Detailed Example
- Lab Exercises

Medium Access Control Overview

What is a MAC?

User
1

User
2

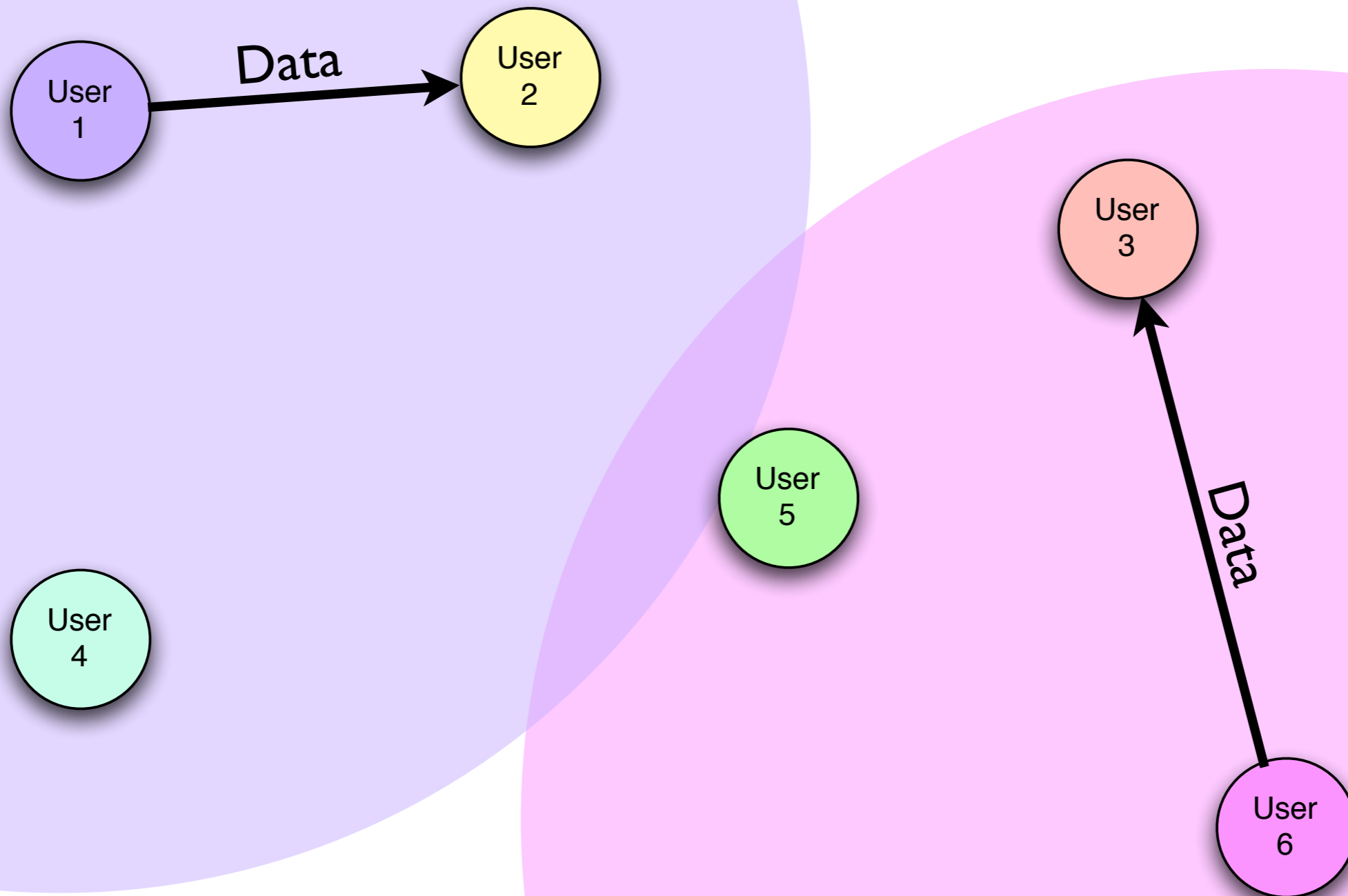
User
3

User
5

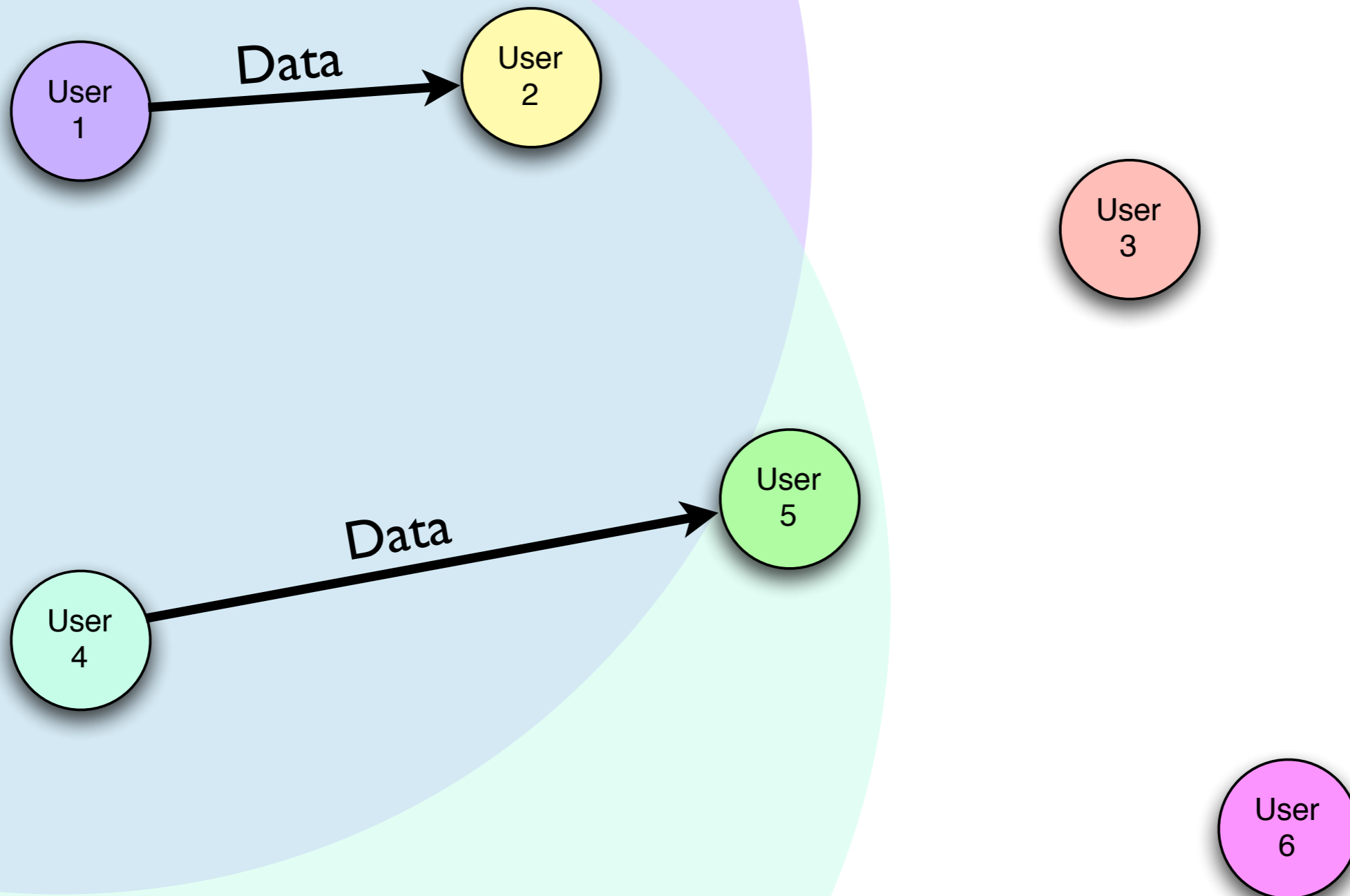
User
4

User
6

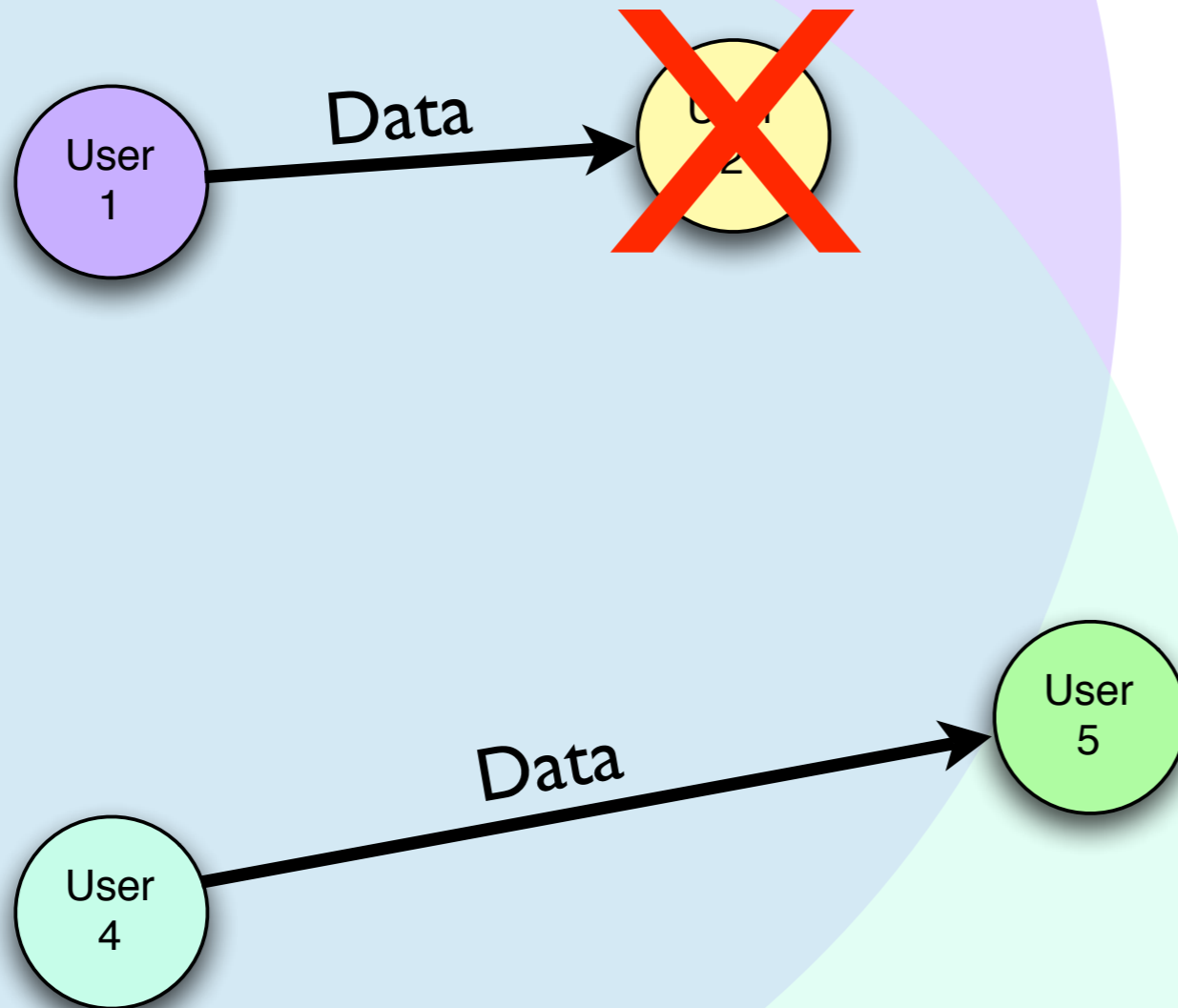
What is a MAC?



What is a MAC?

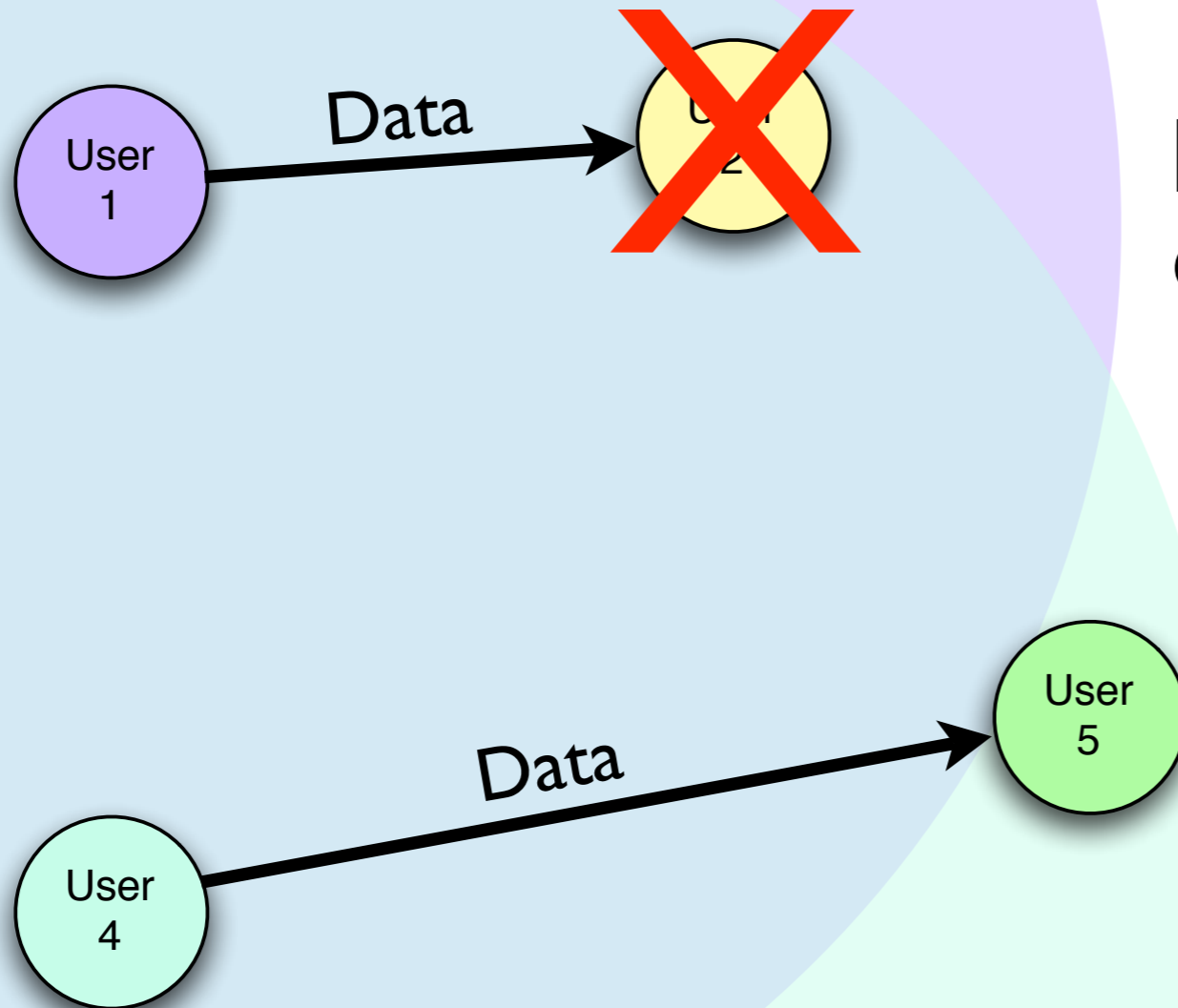


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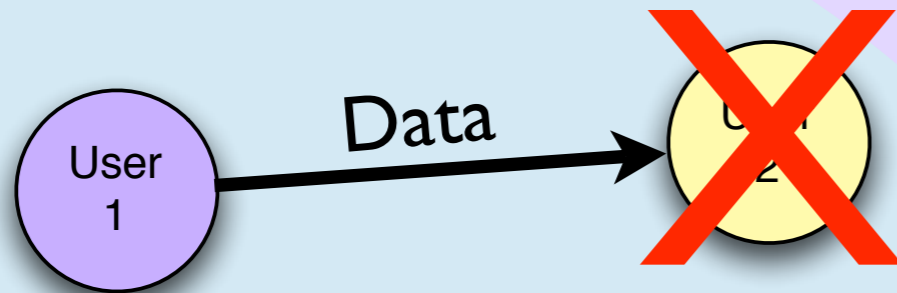


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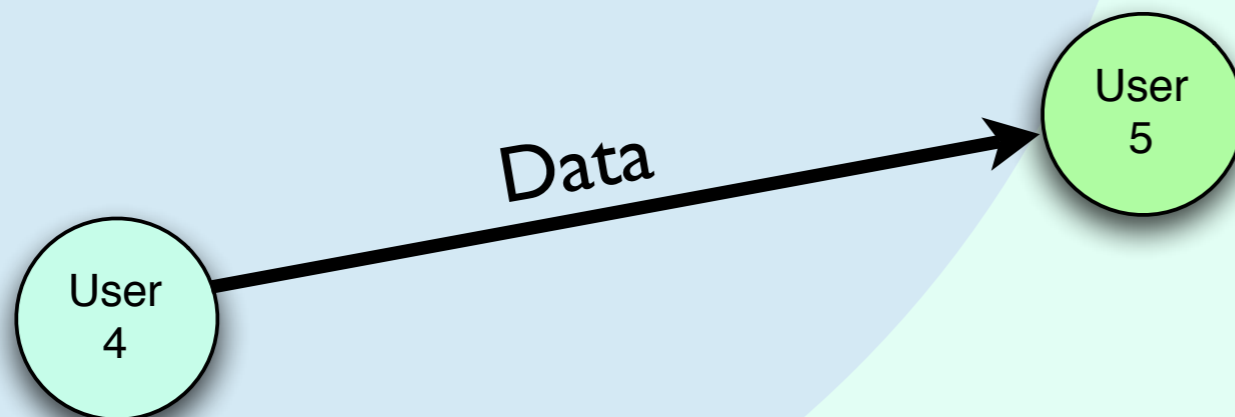
Received a jumbled packet... infer a packet collision



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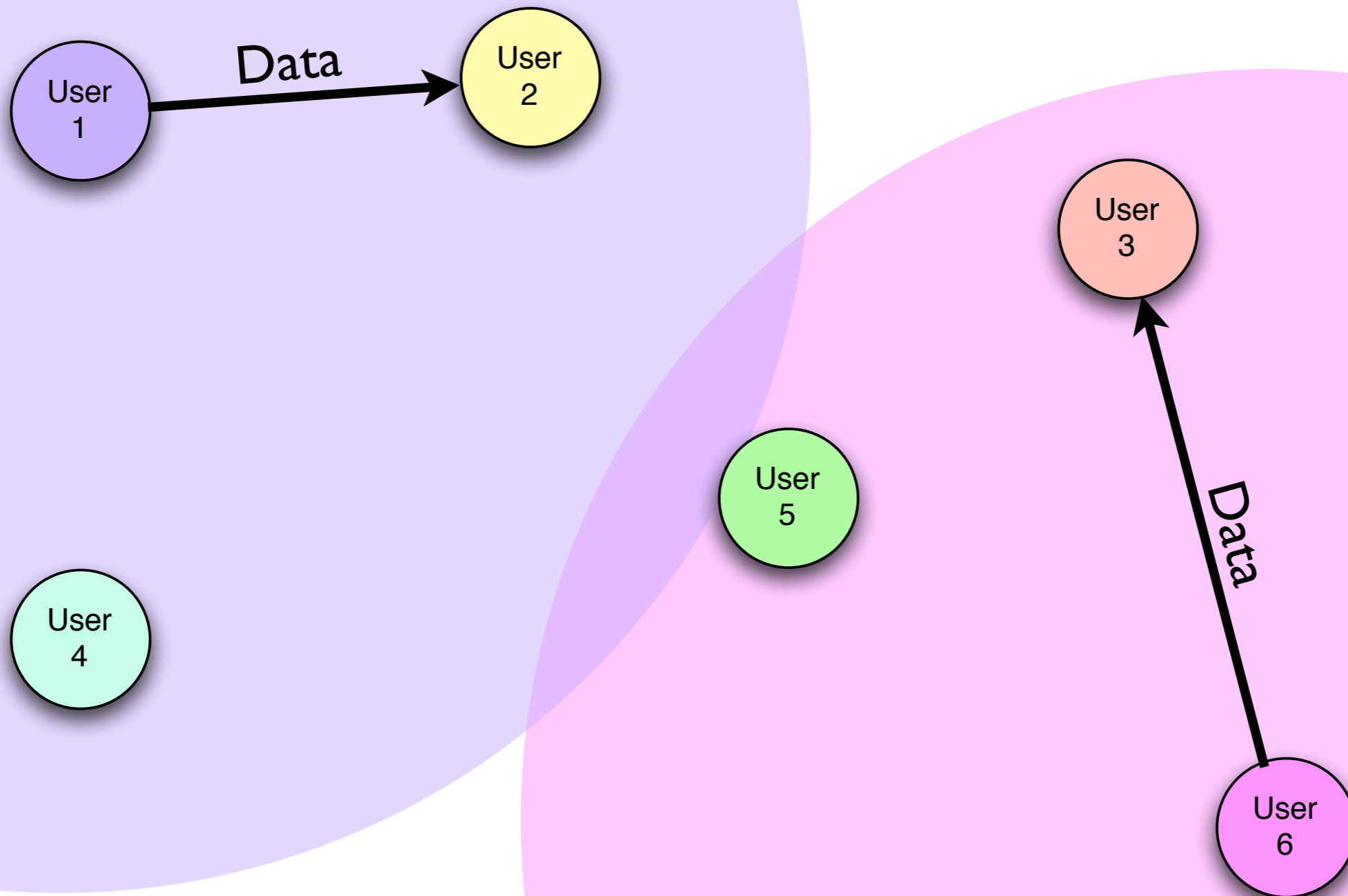


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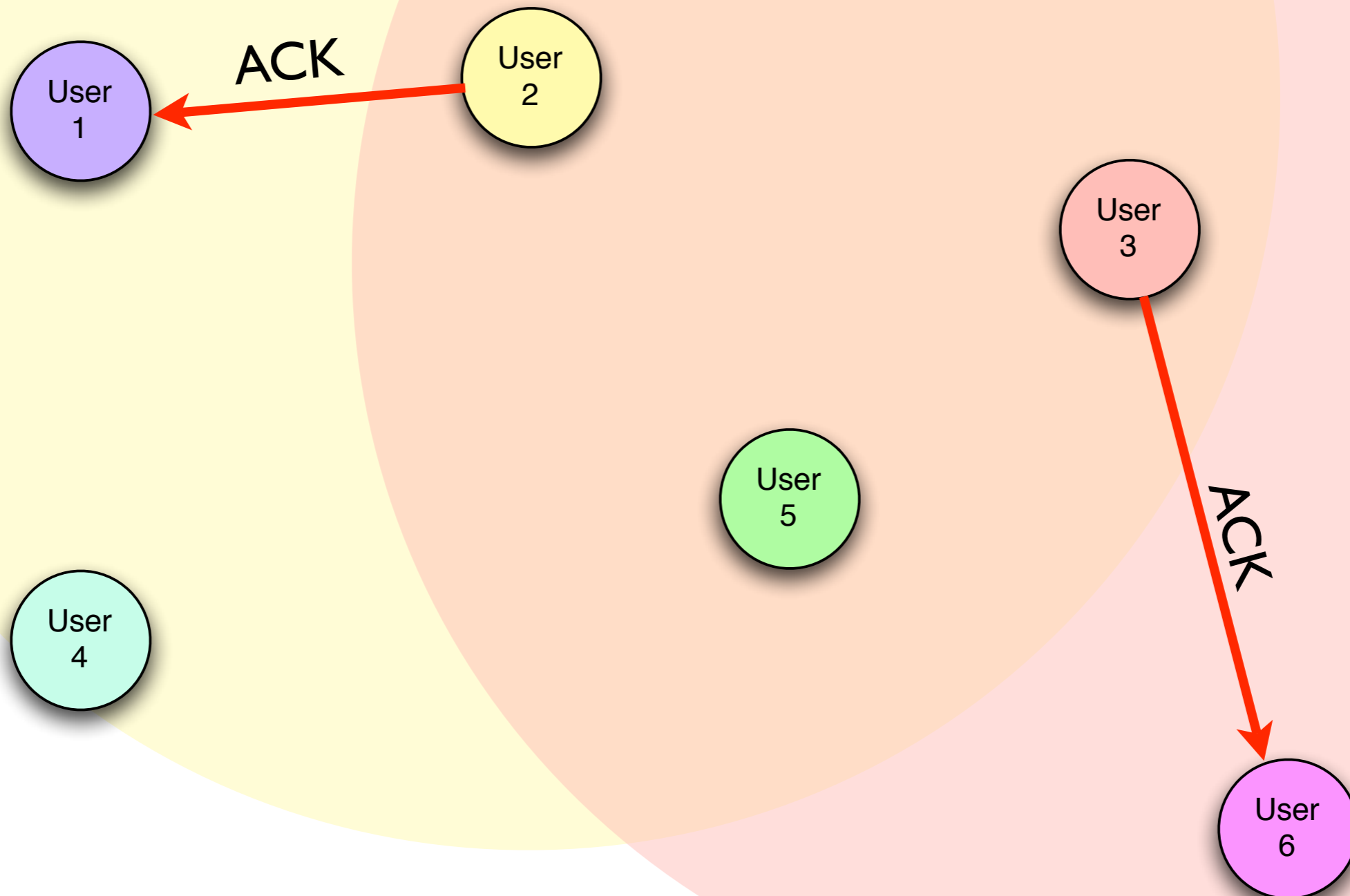


What if we ACK every transmit, and retransmit when we receive no ACK?

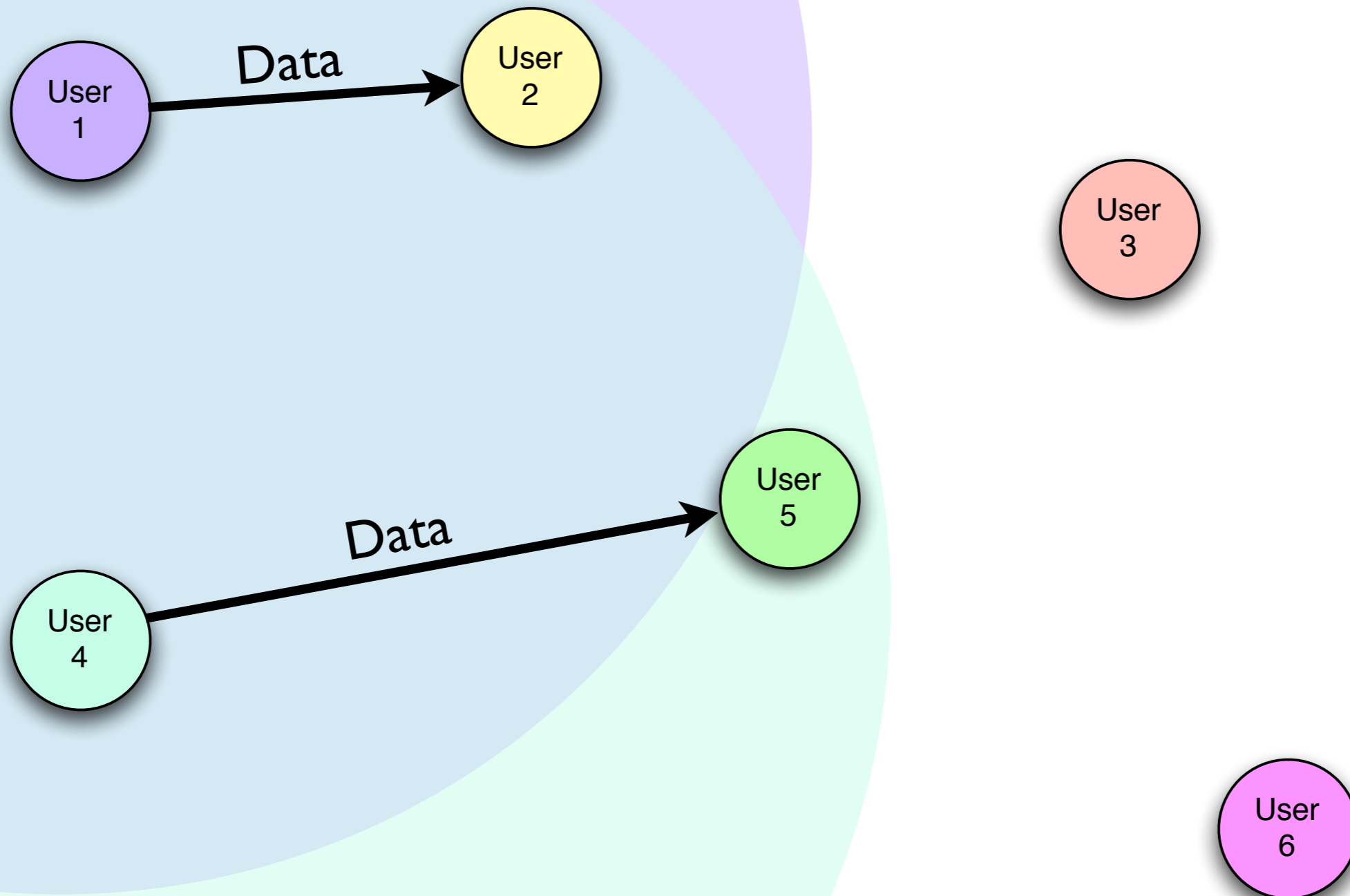
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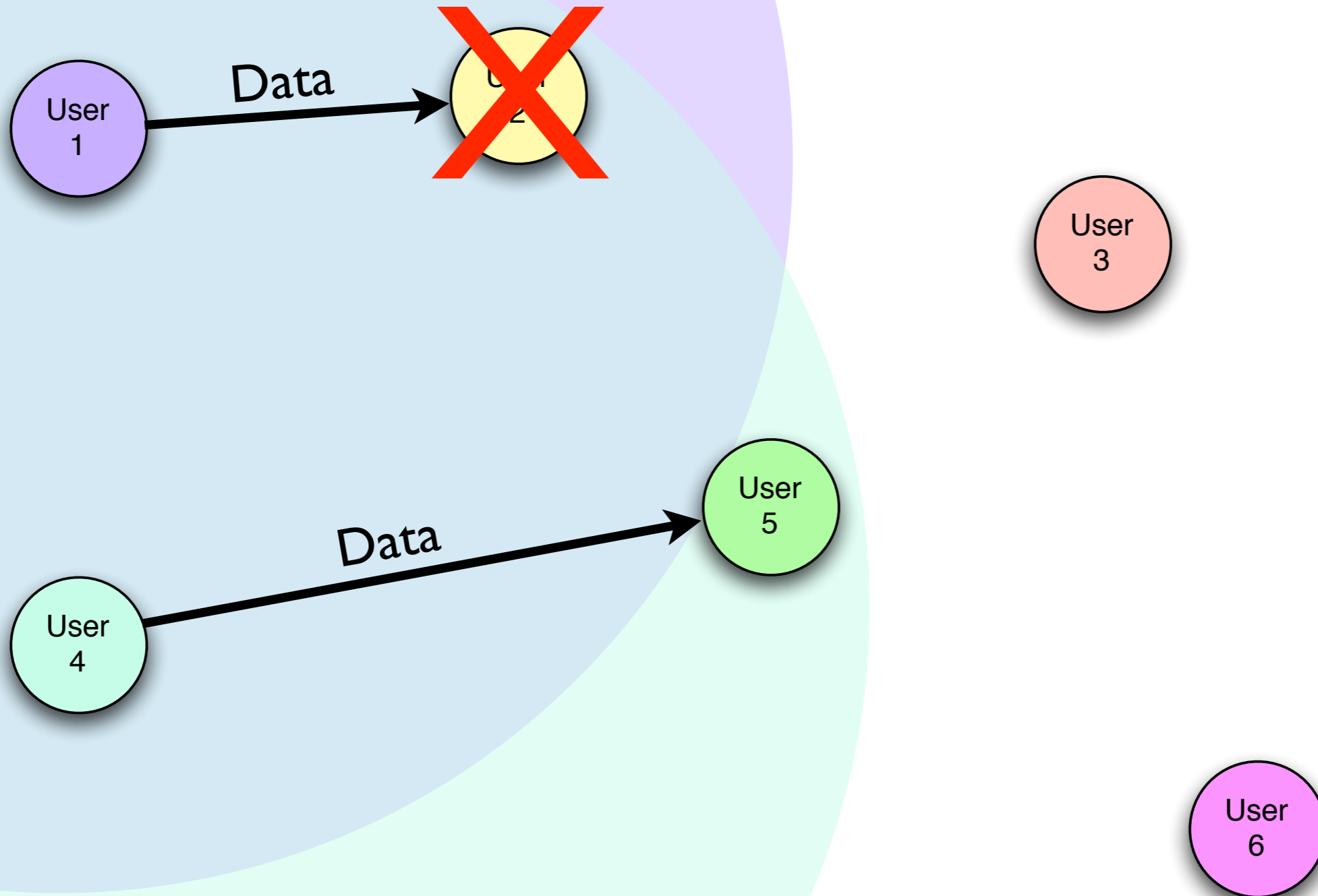
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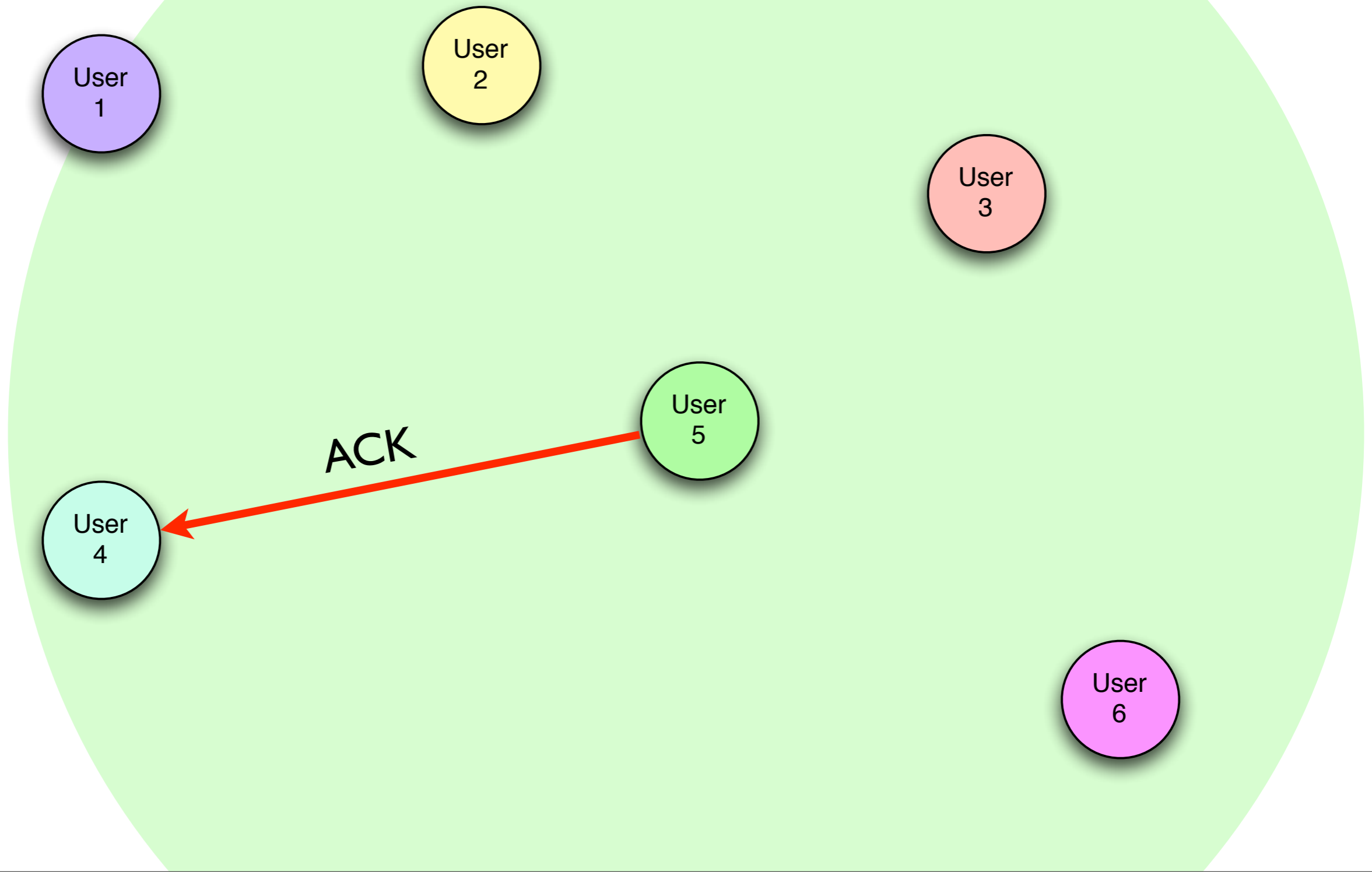
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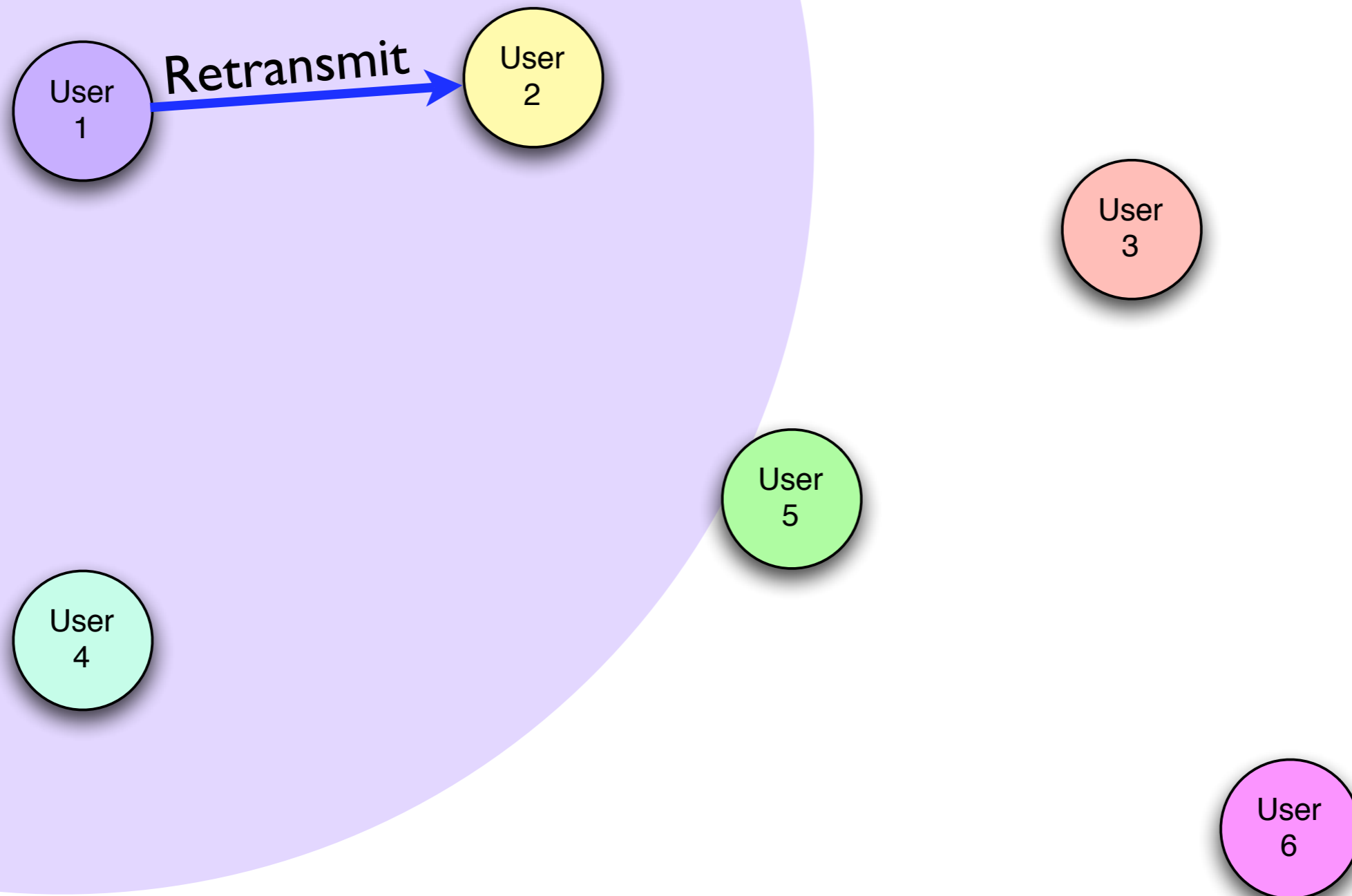
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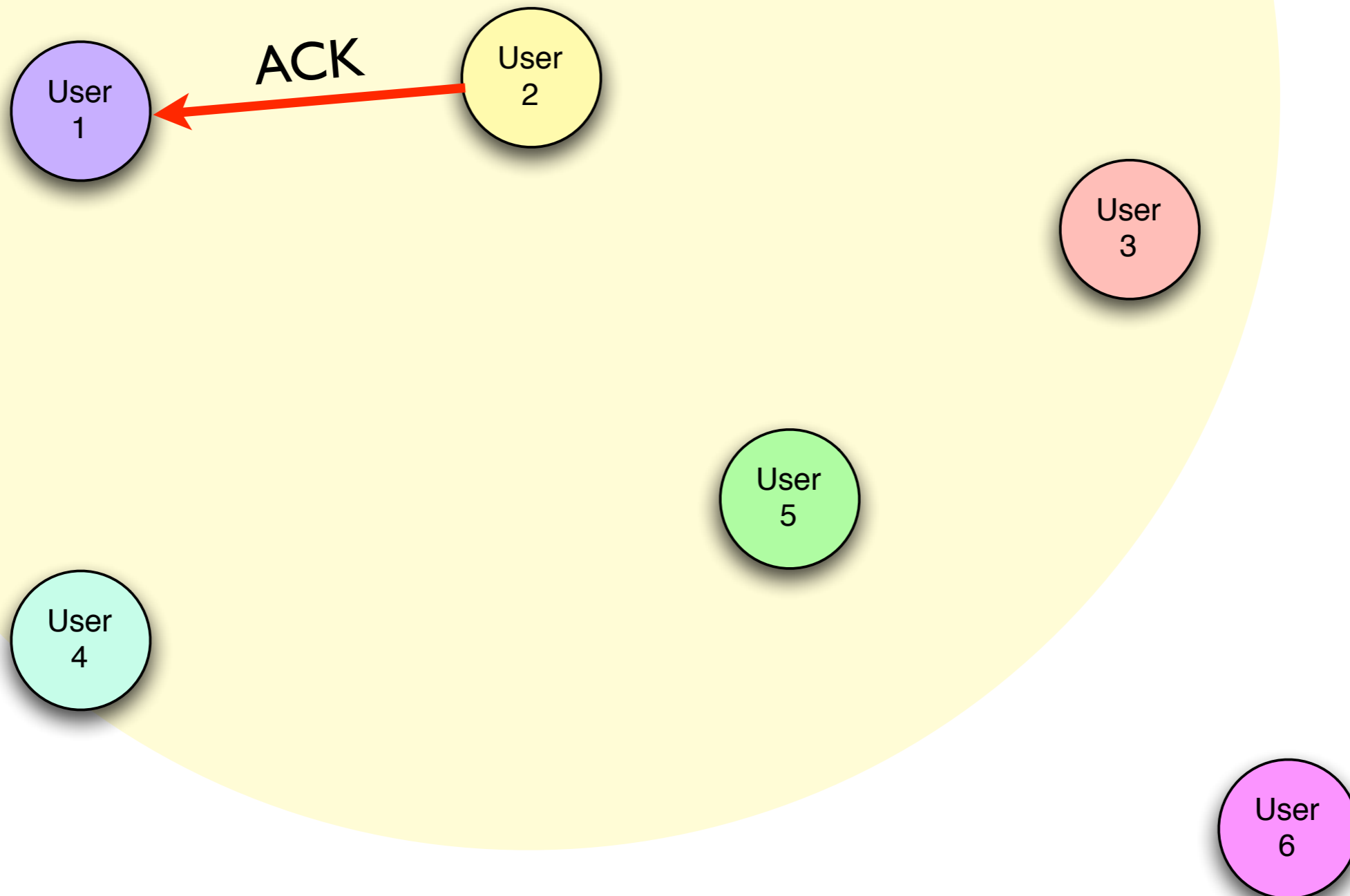
What is a MAC?



What is a MAC?

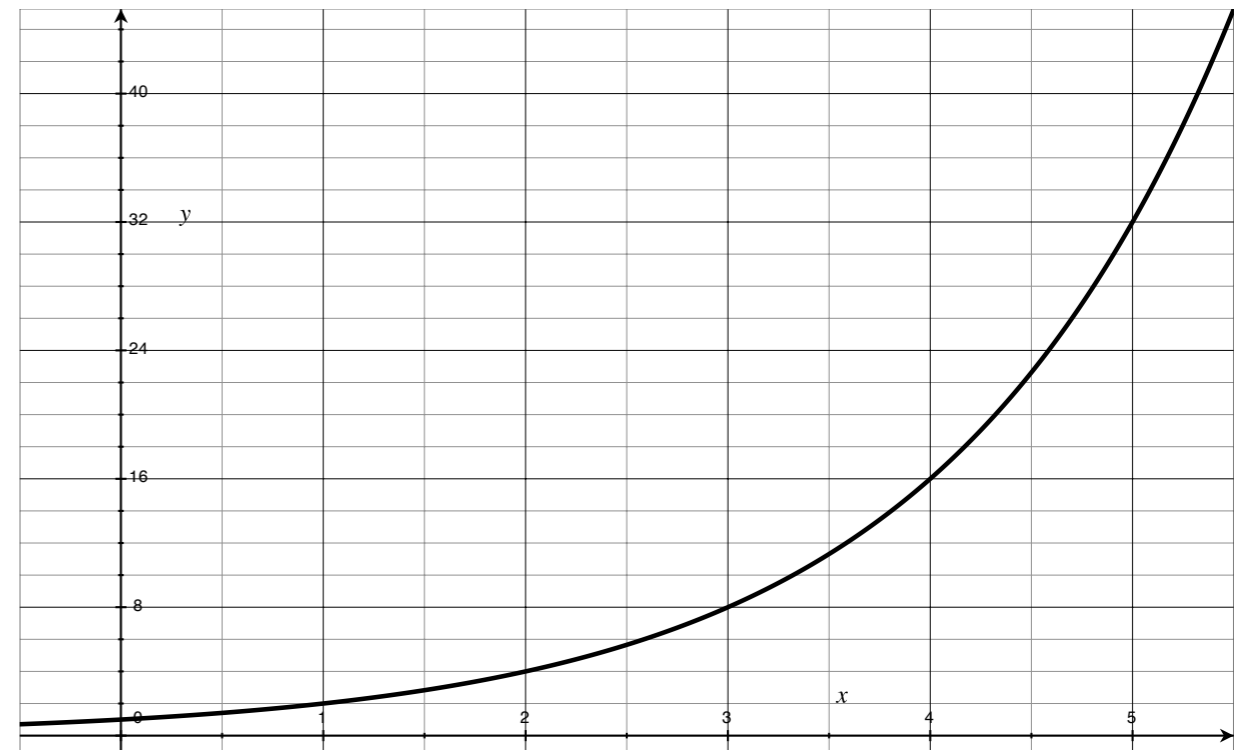


What is a MAC?



Random Backoffs

- **PROBLEM:**
Retransmissions can collide *ad infinitum!*
- **SOLUTION:** Wait a random amount of time before a retransmit



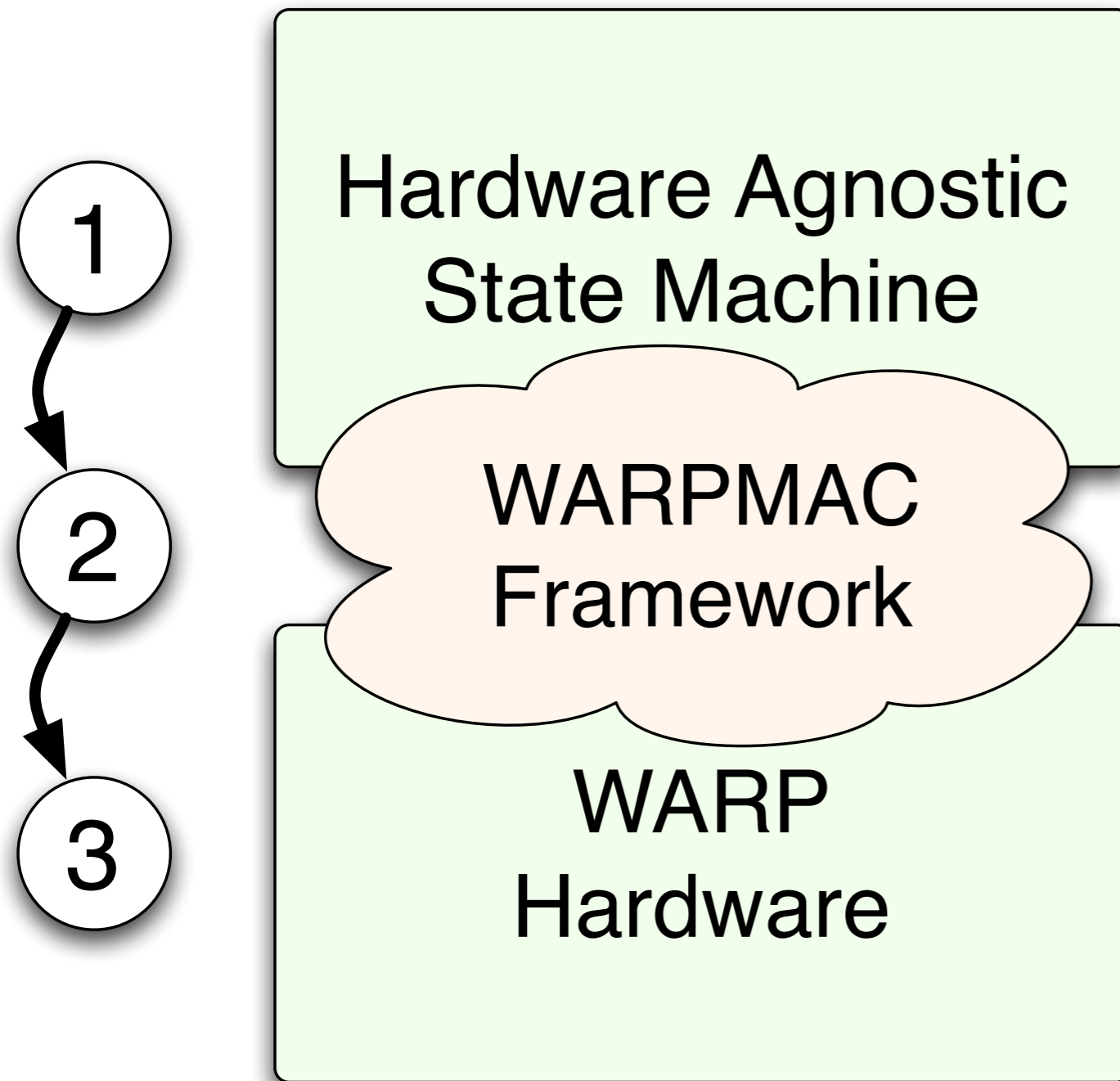
Contention Window
increases over time

Other Important Details

- Carrier Sense Multiple Access (CSMA)
 - Listen to the medium before sending
- Request to Send / Clear to Send (RTS/CTS)
 - “Reserve” the medium with a short packet before sending a long one

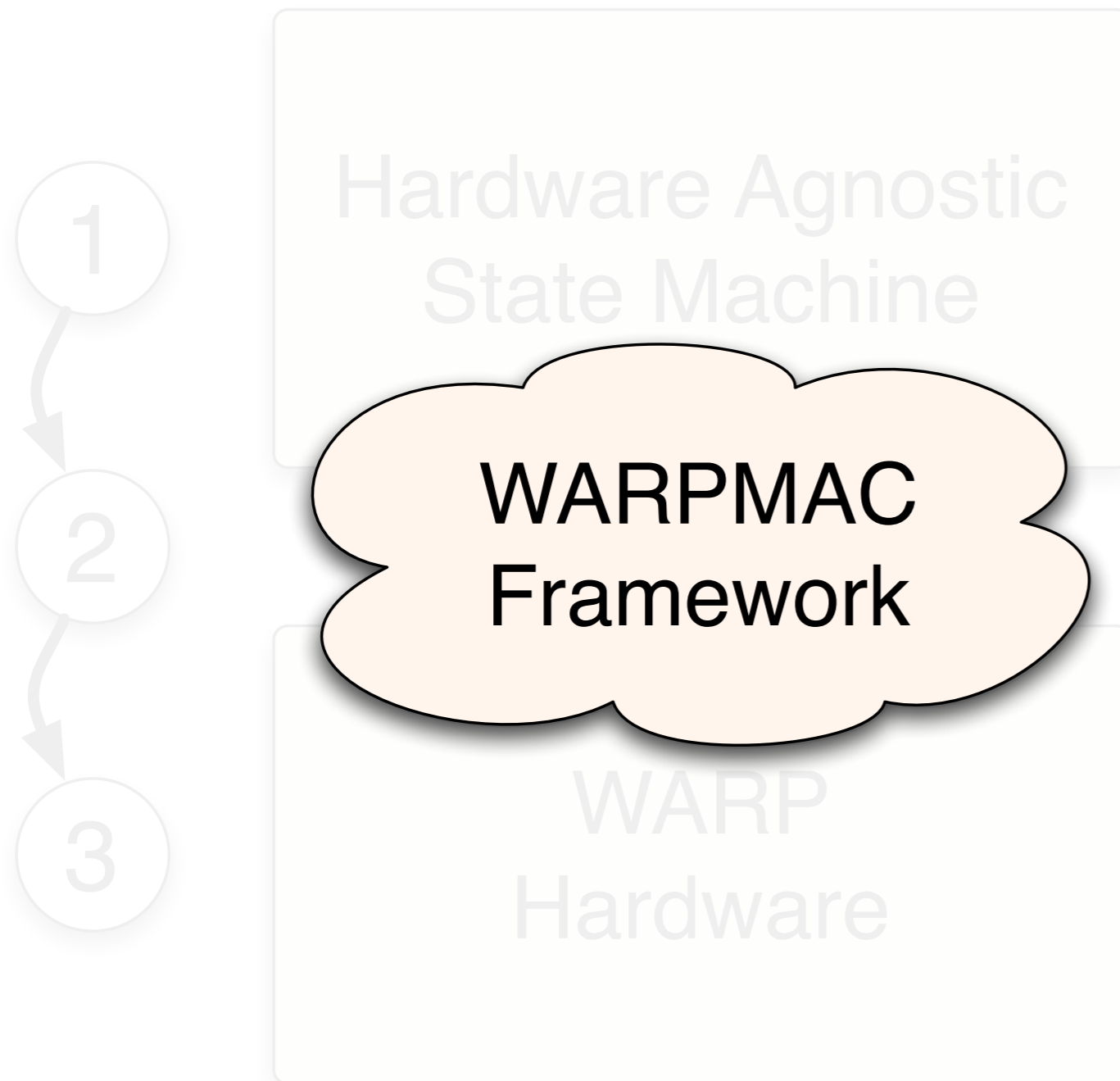
Design Realization

Design Realization



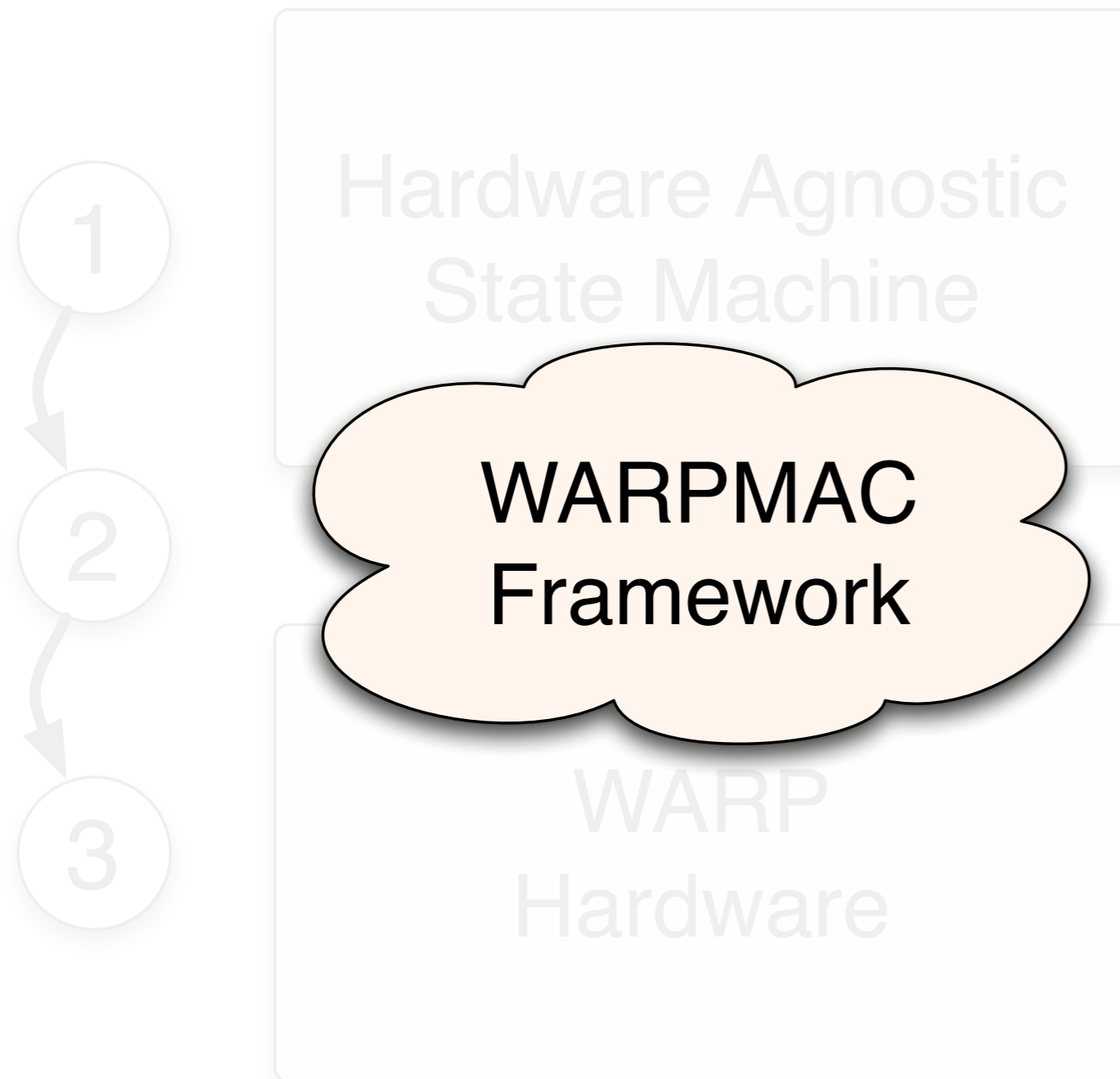
- Program high-level MAC behavior independent of hardware
- Use the WARPMAC framework to stitch the MAC to hardware

Design Realization



- “Driver” analogy is not entirely accurate
- No way to “lock” the framework and have it support all possible future MAC layers

Design Realization

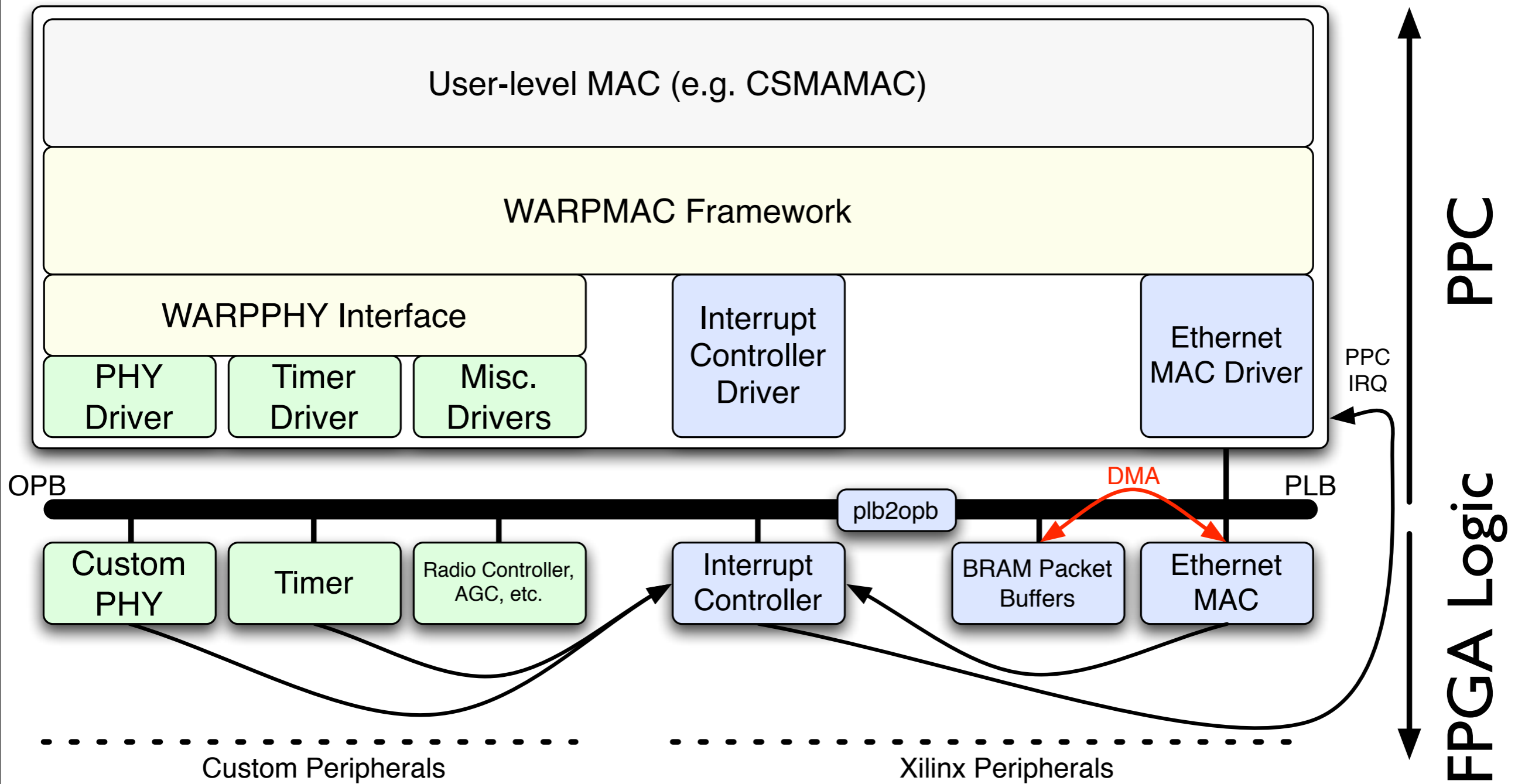


- “Driver” analogy is not entirely accurate
- No way to “lock” the framework and have it support all possible future MAC layers

Solution: *WARPMAC must grow with new algorithms*

WARPMAC Framework

System Diagram





User Code

WARPMAC

WARPPHY

Drivers



User Code

WARPMAC

WARPPHY

Drivers

User Code

WARPMAC

WARPPHY

Drivers

PHY Driver:

- Configure very low-level parameters
 - Correlation thresholds
 - FFT scaling parameters
 - Filter coefficients
 - Etc.

User Code

WARPMAC

WARPPHY

Drivers



User Code

WARPMAC

WARPPHY

Drivers



User Code

WARPMAC

WARPPHY

Drivers

Radio Controller Driver:

- Set center frequency
- Switch from Rx to Tx mode and vice versa





User Code

WARPMAC

WARPPHY

Drivers



User Code

WARPMAC

WARPPHY

Drivers

PHY Control:

- Provides control over PHY commonalities
 - General initialization command
 - Configure constellation order
 - “Start” and “Stop” the PHY

User Code

WARPMAC

WARPPHY

Drivers



User Code

WARPMAC

WARPPHY

Drivers

User Code

WARPMAC

WARPPHY

Drivers

**Mostly PHY
agnostic**

User Code

WARPMAC

**Completely PHY
dependent**

WARPPHY

Drivers

User Code

WARPMAC

WARPPHY

Drivers

MAC Control:

- Provides control over MAC commonalities
 - Timers for timeouts, backoffs, etc.
 - Carrier-sensing functions
 - Register user callbacks to ISRs
 - Etc.

User Code

WARPMAC

WARPPHY

Drivers

User Code

WARPMAC

WARPPHY

Drivers

User Code

WARPMAC

WARPPHY

Drivers

User-level MAC Algorithms:

- High-level MAC algorithms
- Some examples so far:
 - Aloha
 - Carrier-sensing MAC
 - Opportunistic Auto-Rate (OAR)
 - MAC Workshop Exercises

User Code

WARPMAC

WARPPHY

Drivers



User Code

WARPMAC

WARPPHY

Drivers

An example: ALOHA

- Simple, usable MAC
- Serves as a foundation for a large class of other random access protocols
- The algorithm is simple:

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Received a packet? Send an ACK

An example: ALOHA

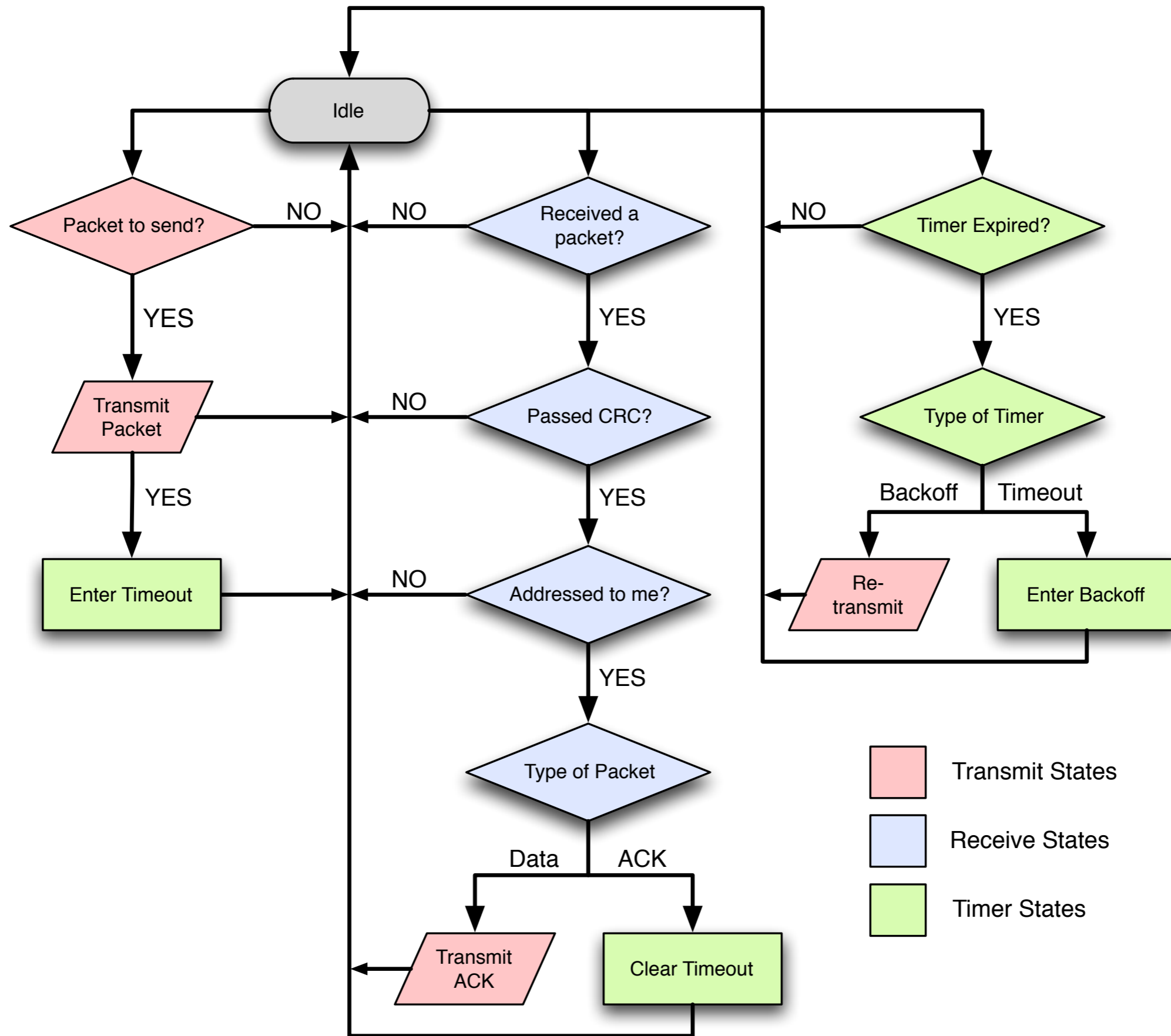
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Packet to send? Just send it

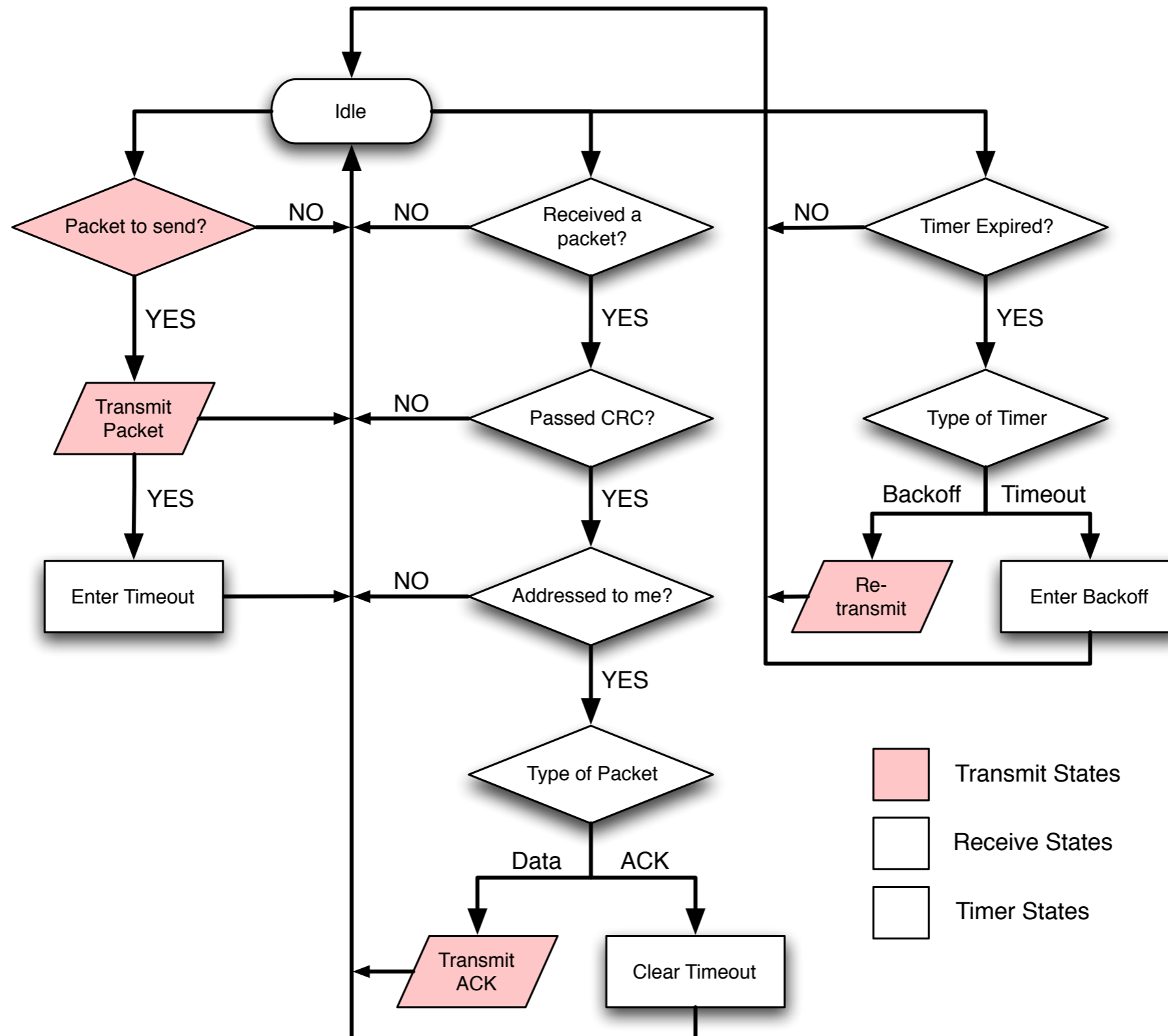
Received a packet? Send an ACK

Received no ACK? Backoff and resend

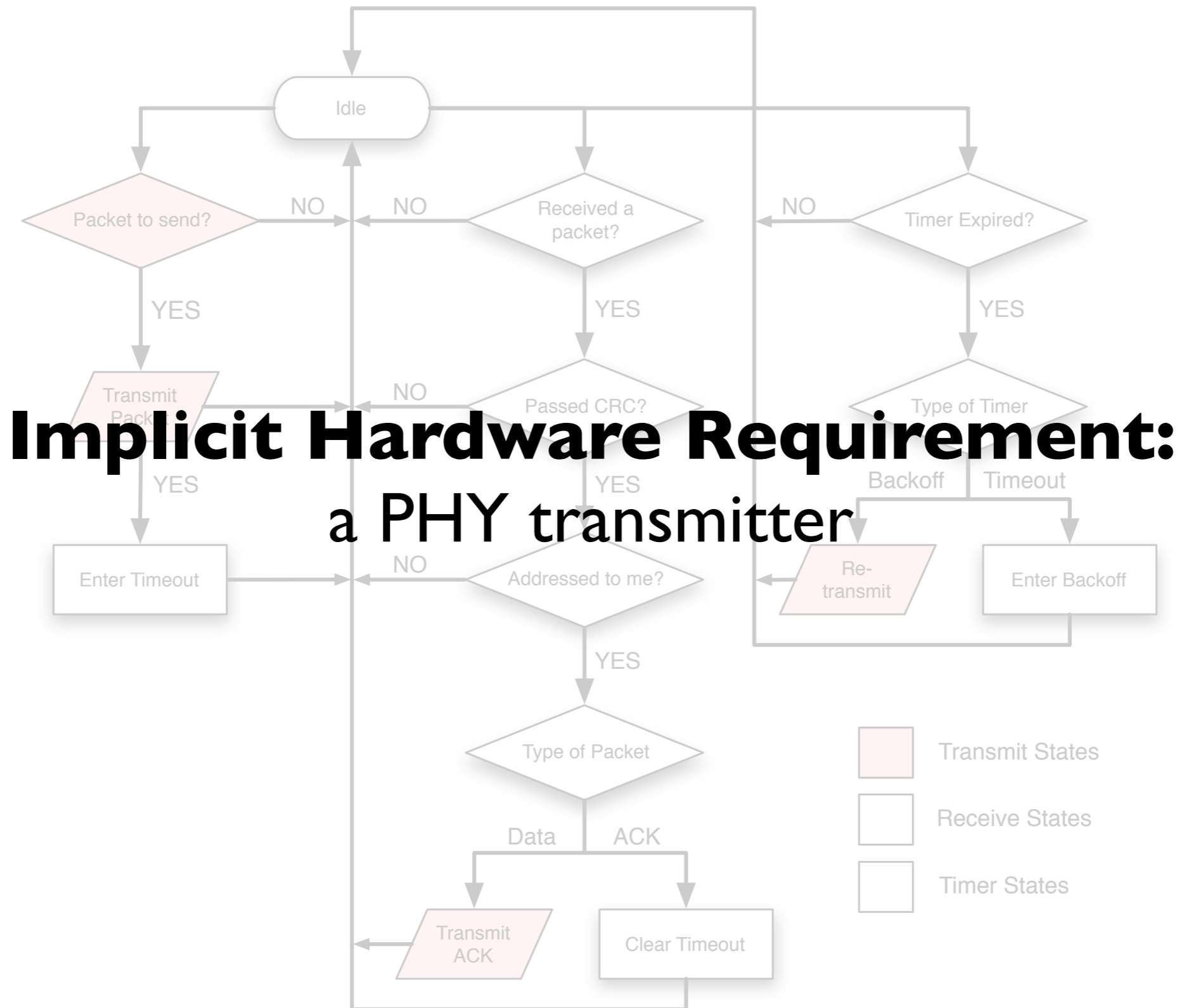
An example: ALOHA



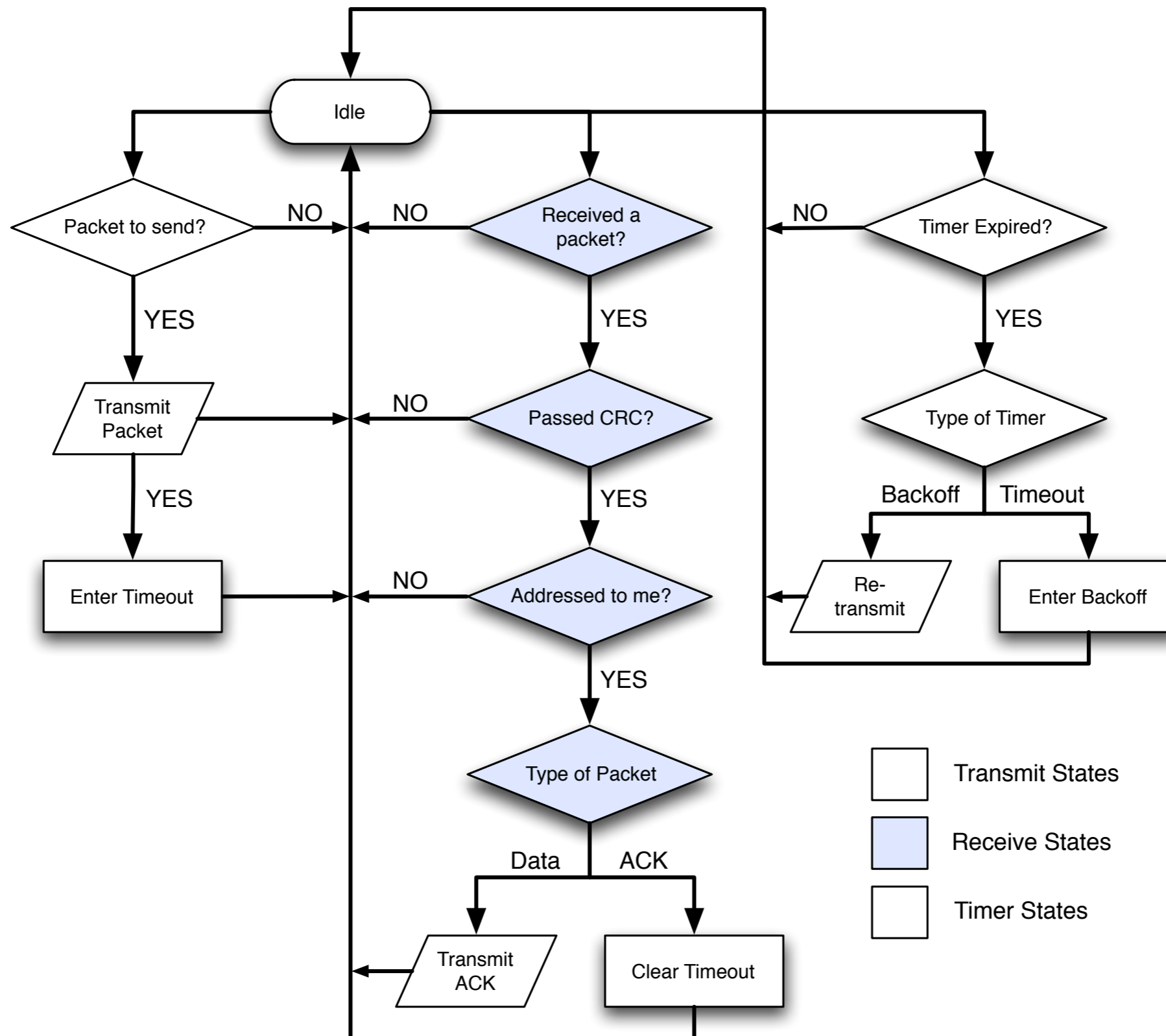
An example: ALOHA



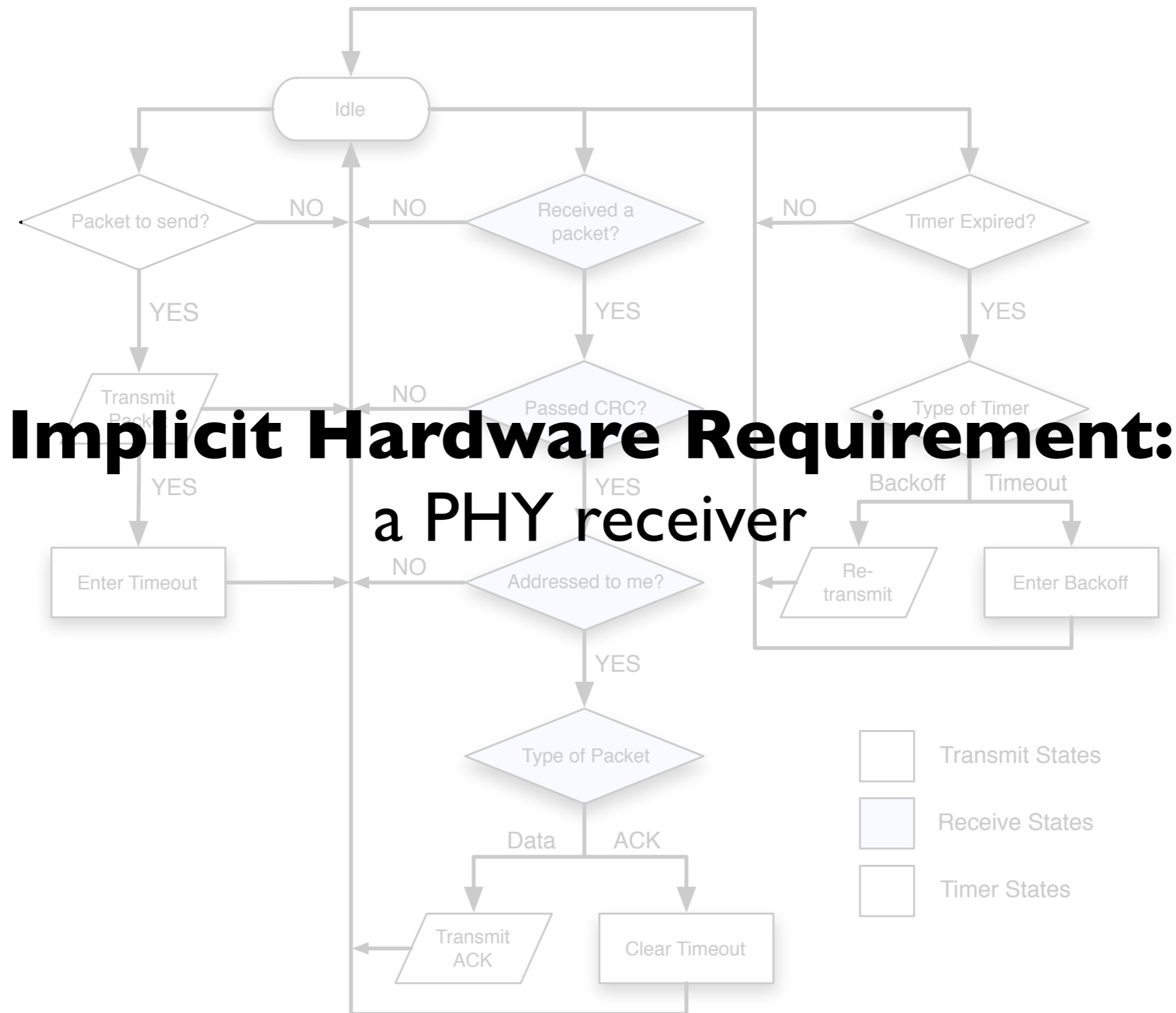
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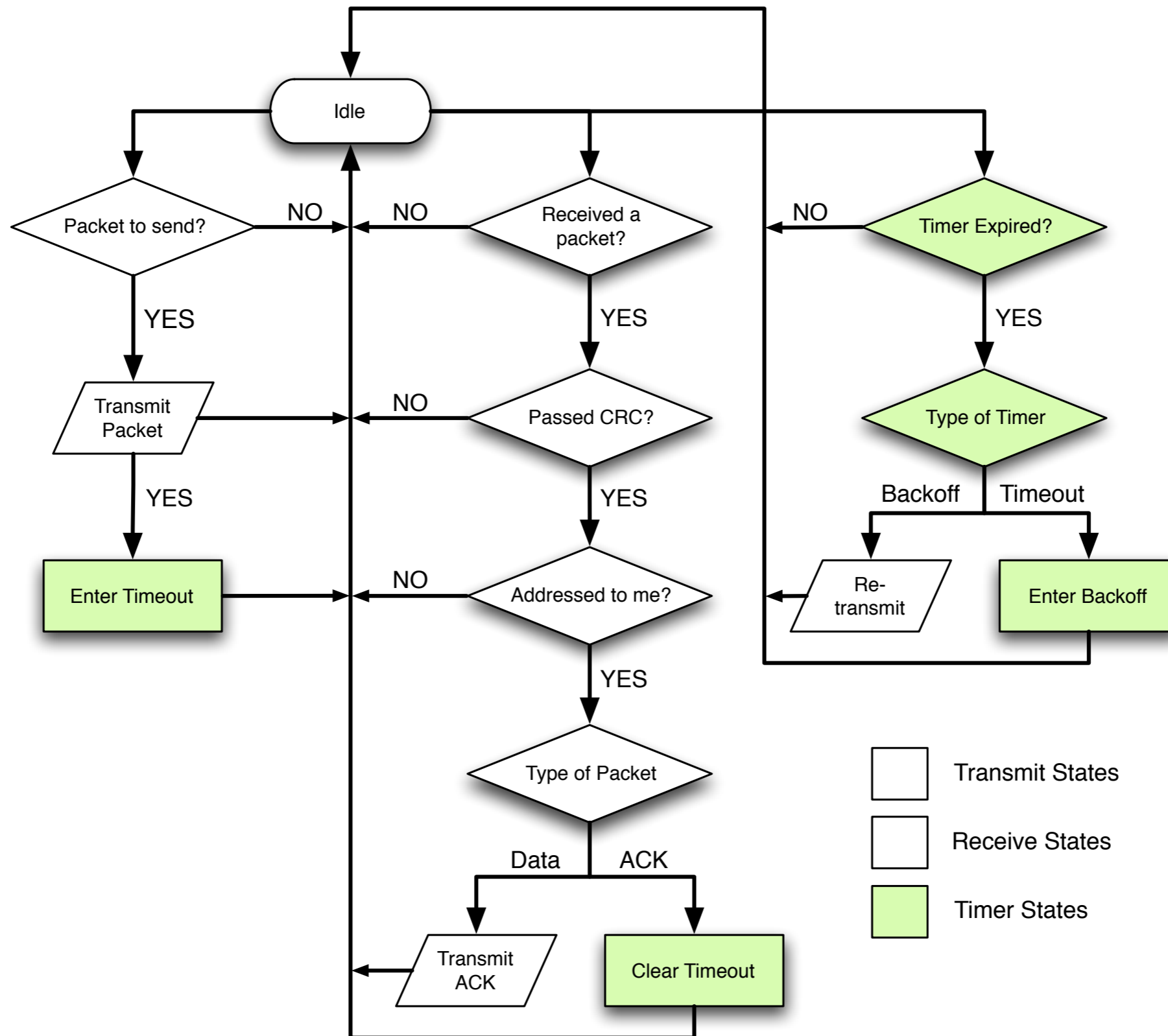
An example: ALOHA



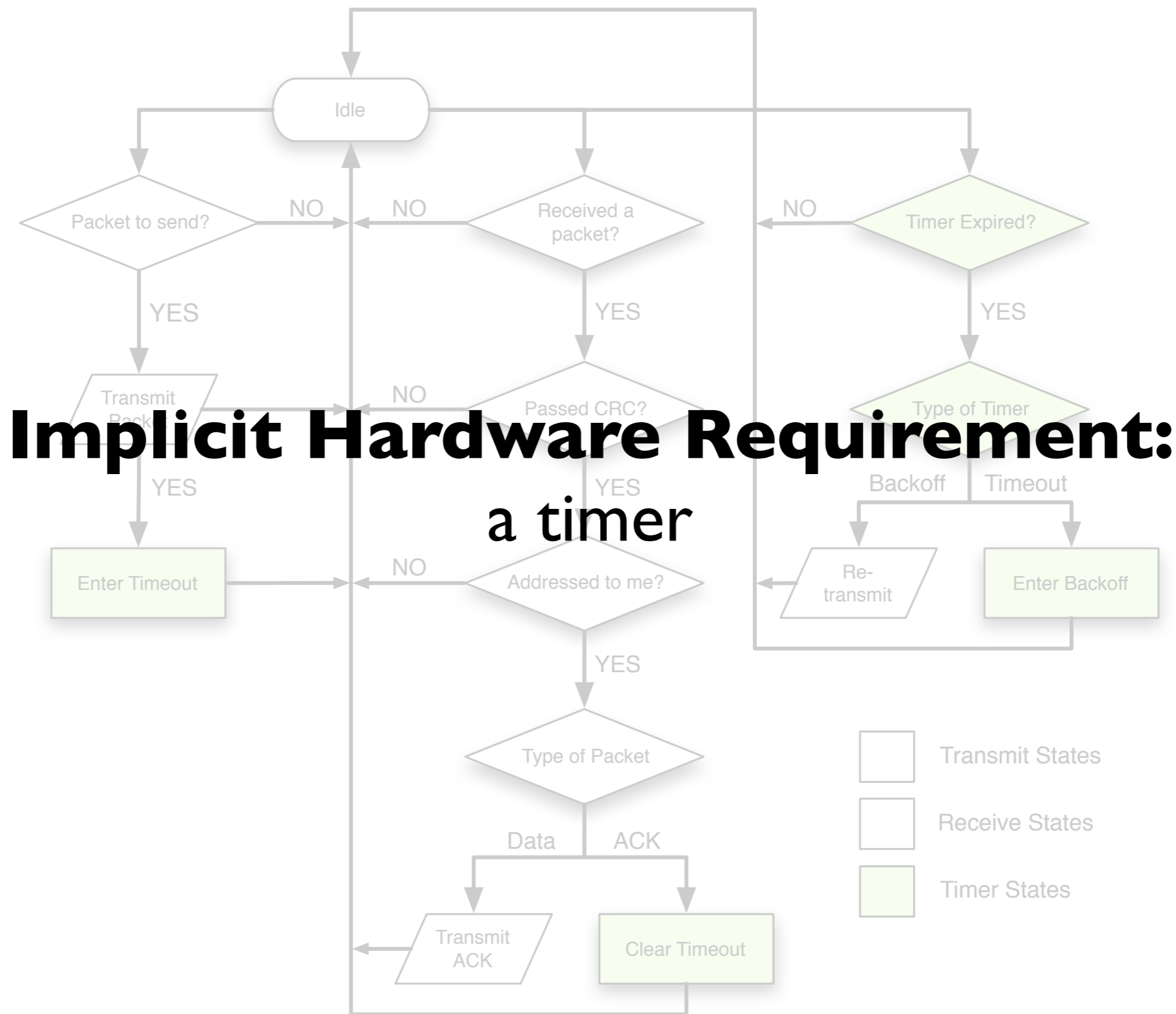
An example: ALOHA



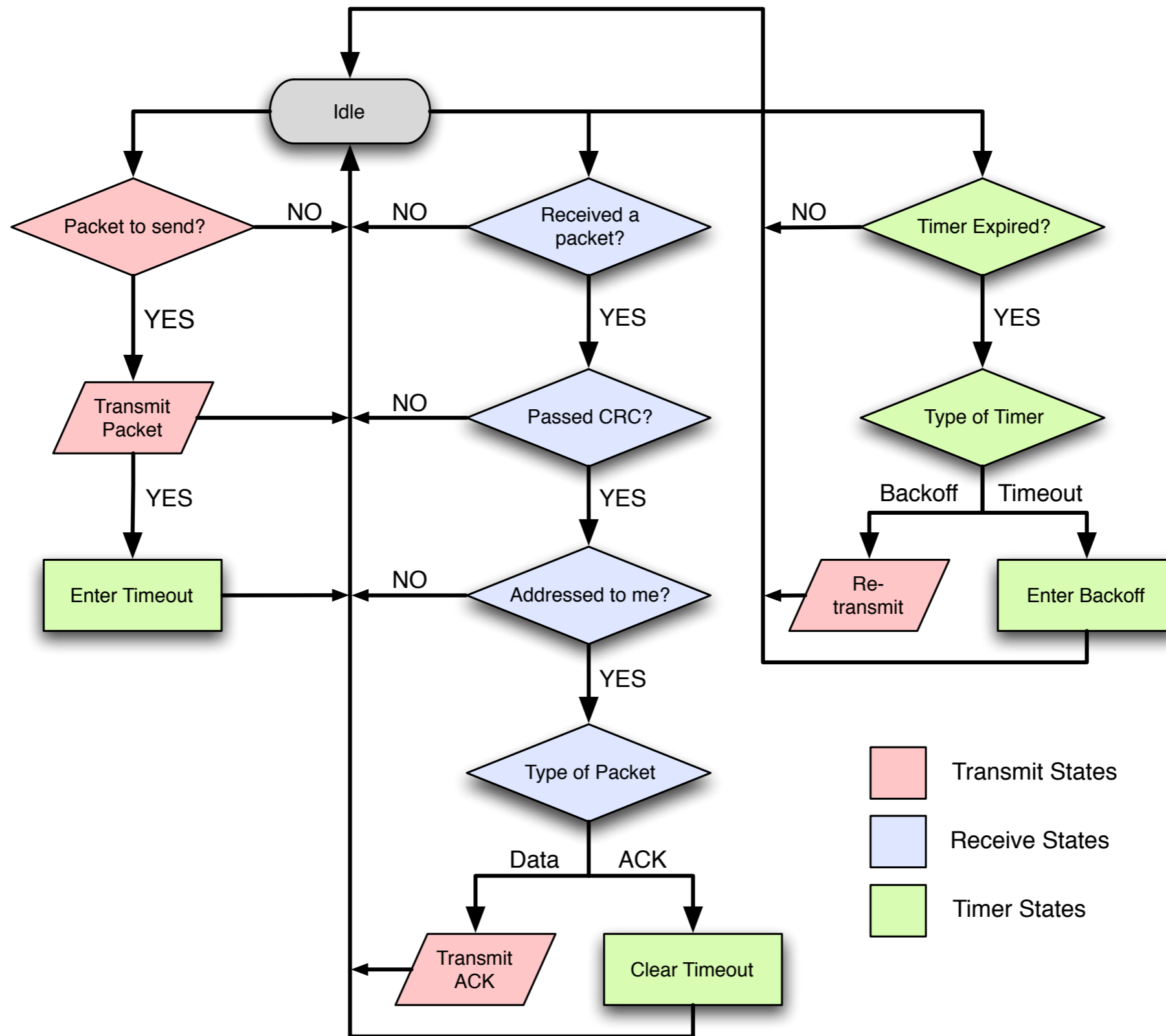
An example: ALOHA



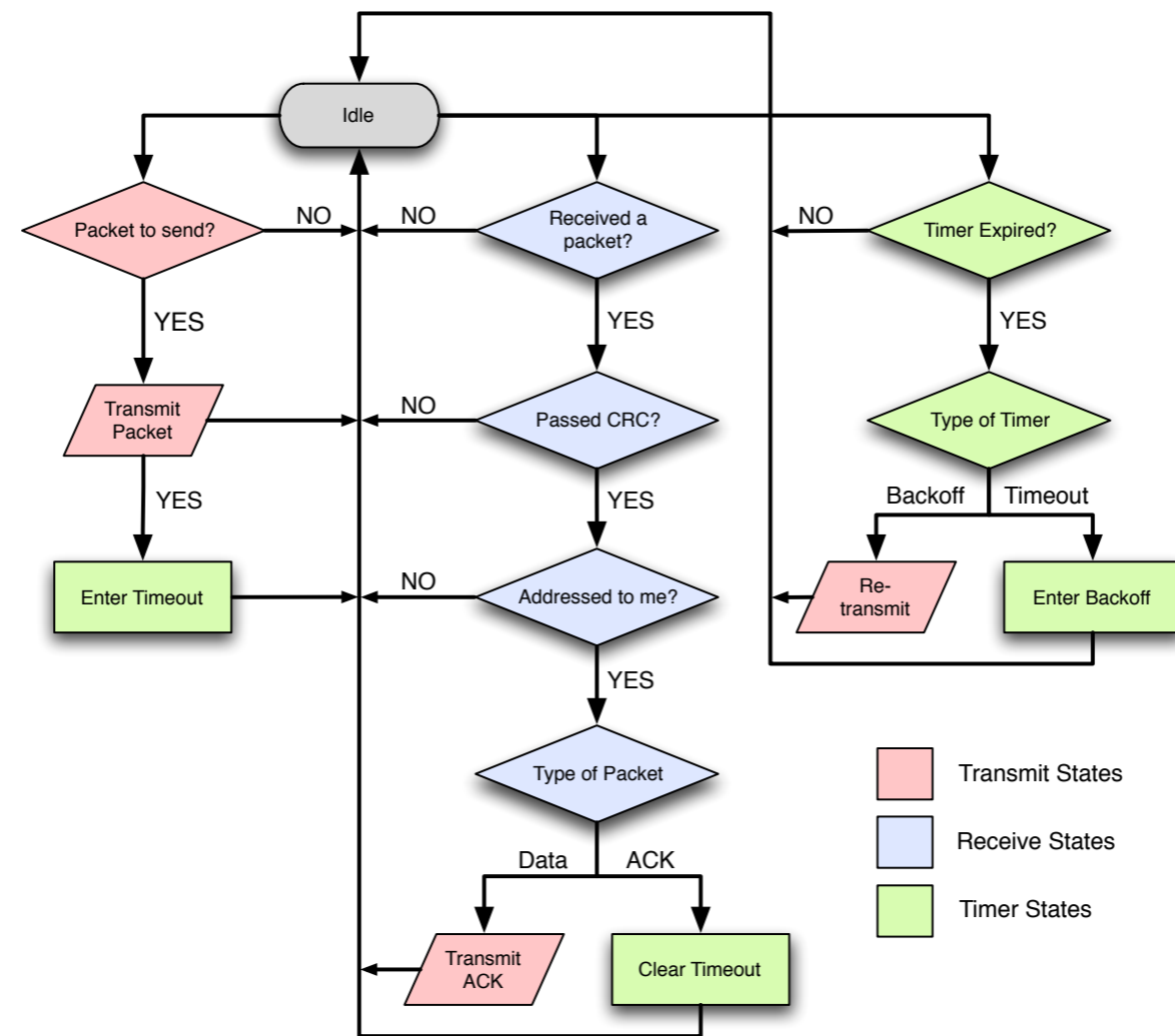
An example: ALOHA



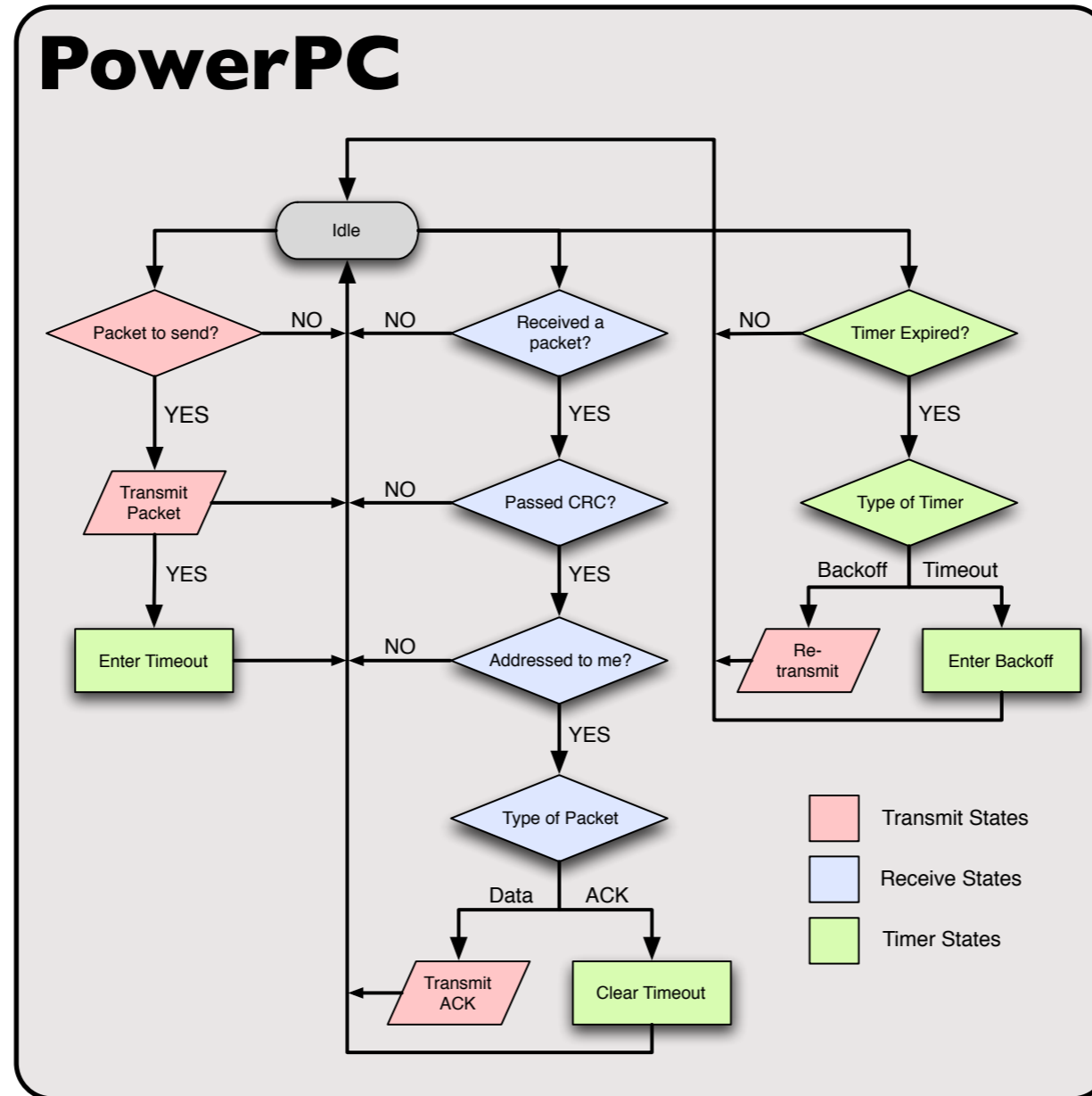
An example: ALOHA



Hardware Requirements



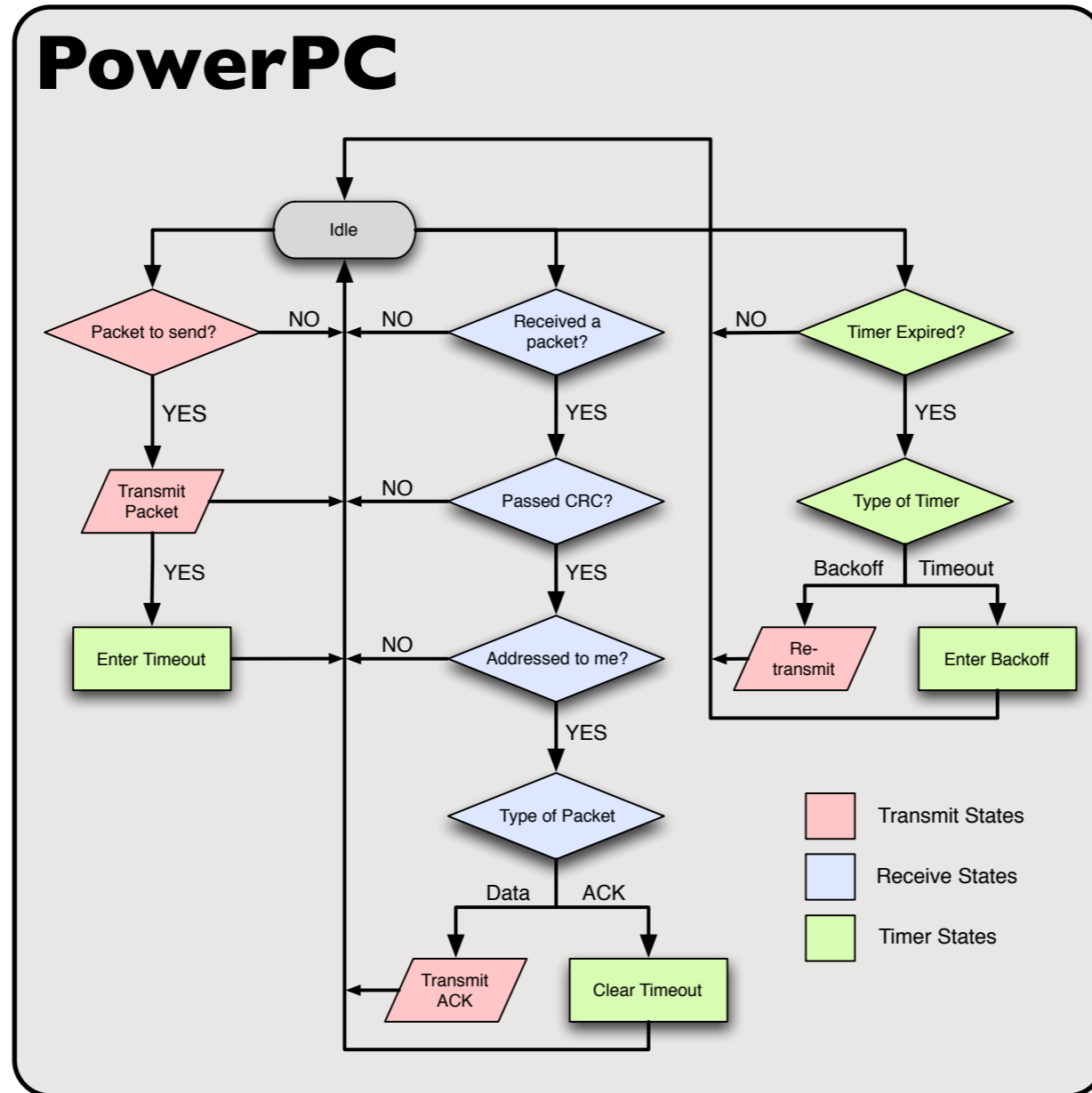
Hardware Requirements



Hardware Requirements

PHY Transmitter

PHY Receiver



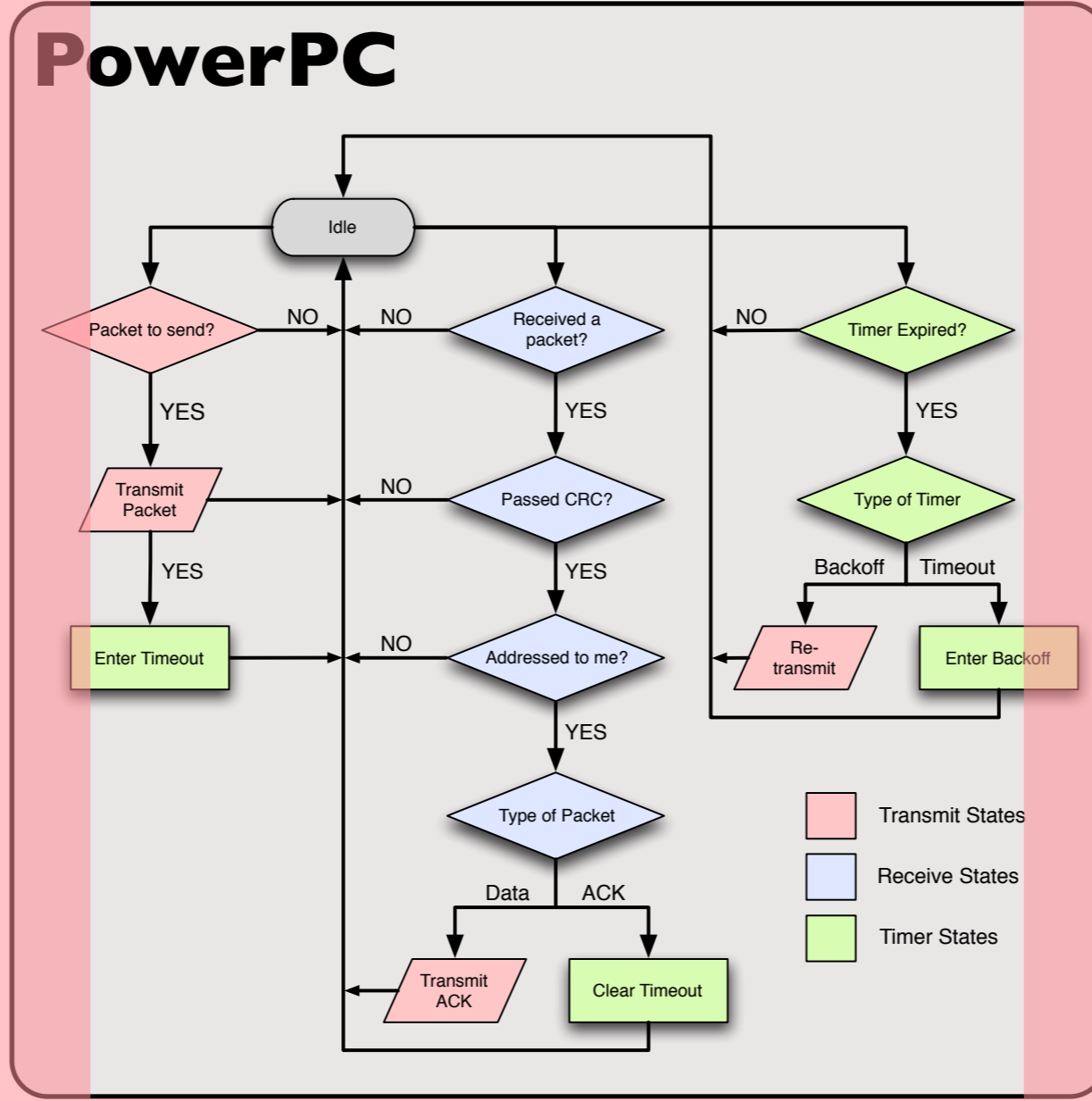
Hardware Timer

Ethernet

Hardware Requirements

PHY Transmitter

PHY Receiver



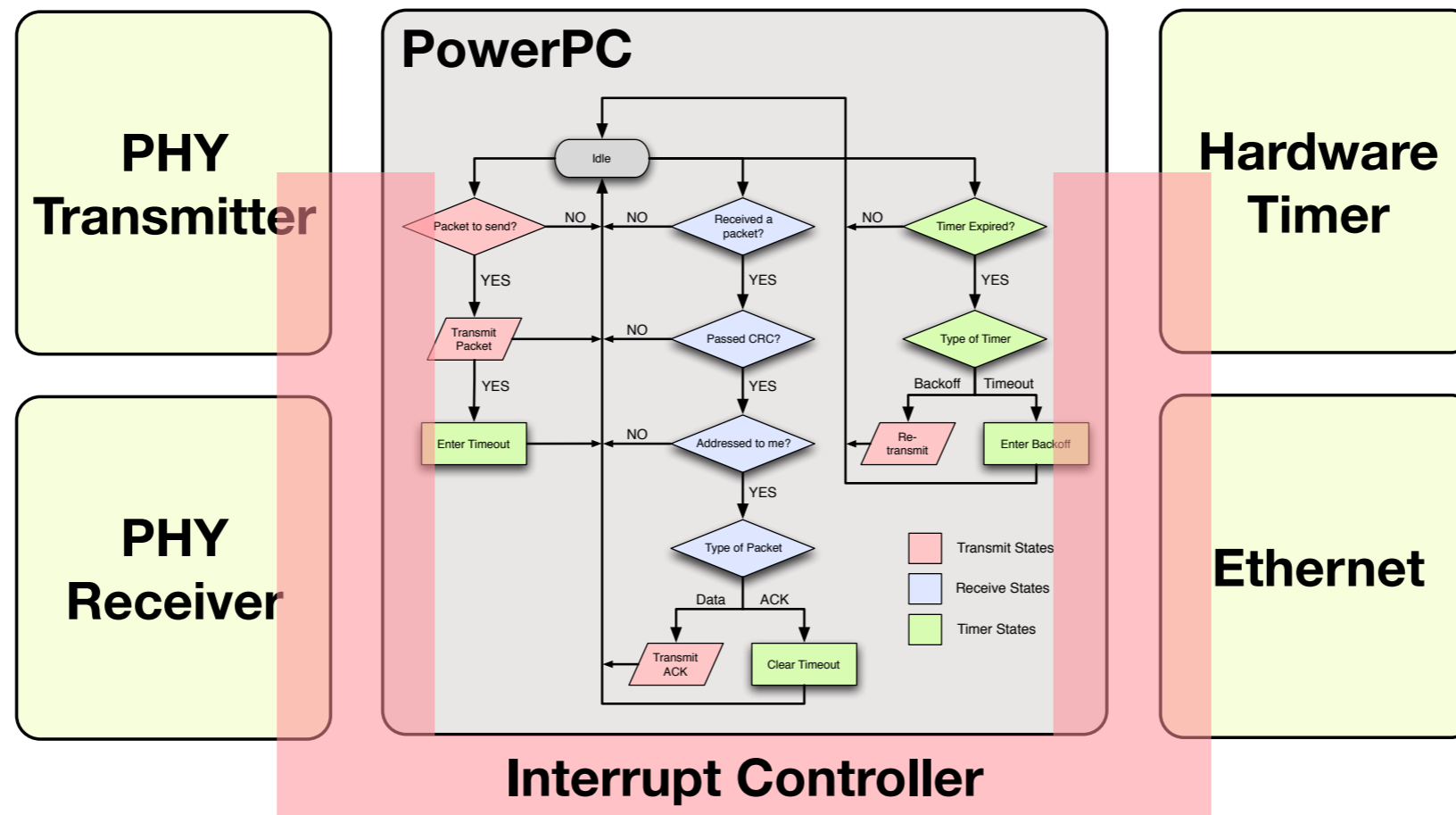
Hardware Timer

Ethernet

Interrupt Controller

Hardware Platform

Hardware Platform



Hardware Platform

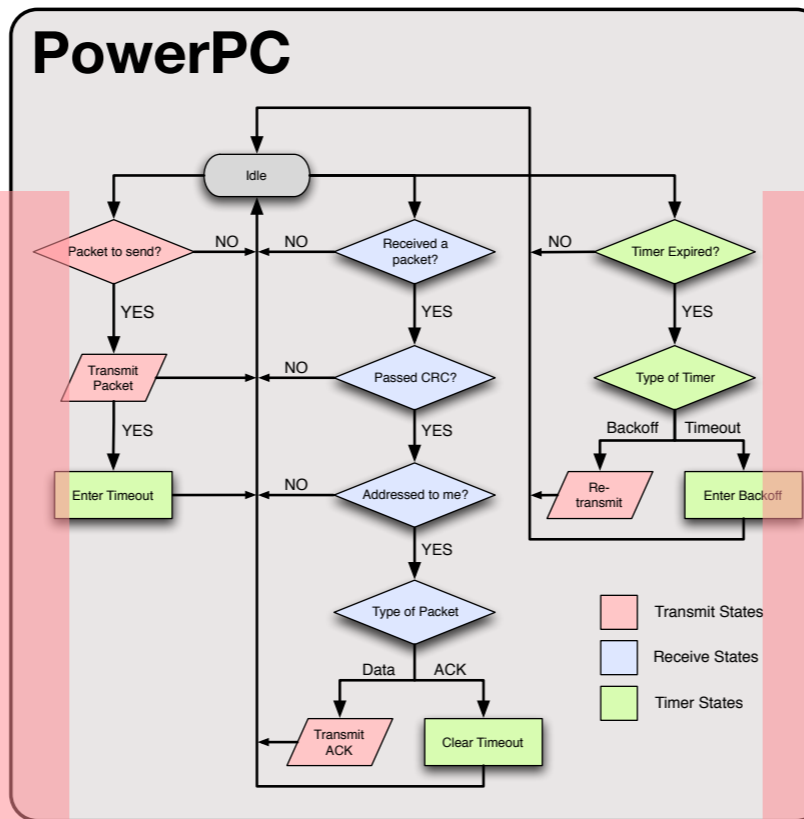
**Radio
Controller**

**Packet
Detection**

**Automatic
Gain
Control**

**PHY
Transmitter**

**PHY
Receiver**



Interrupt Controller

**Hardware
Timer**

Ethernet

Hardware Platform

Radio
Controller

Packet
Detection

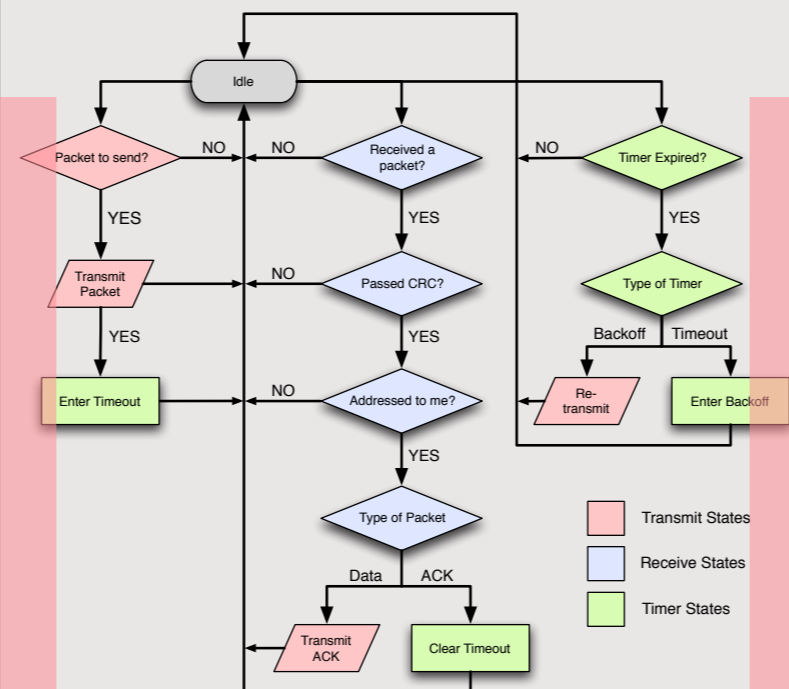
Automatic
Gain
Control

Push-
buttons

PHY
Transmitter

PHY
Receiver

PowerPC



Interrupt Controller

Hardware
Timer

Ethernet

LEDs

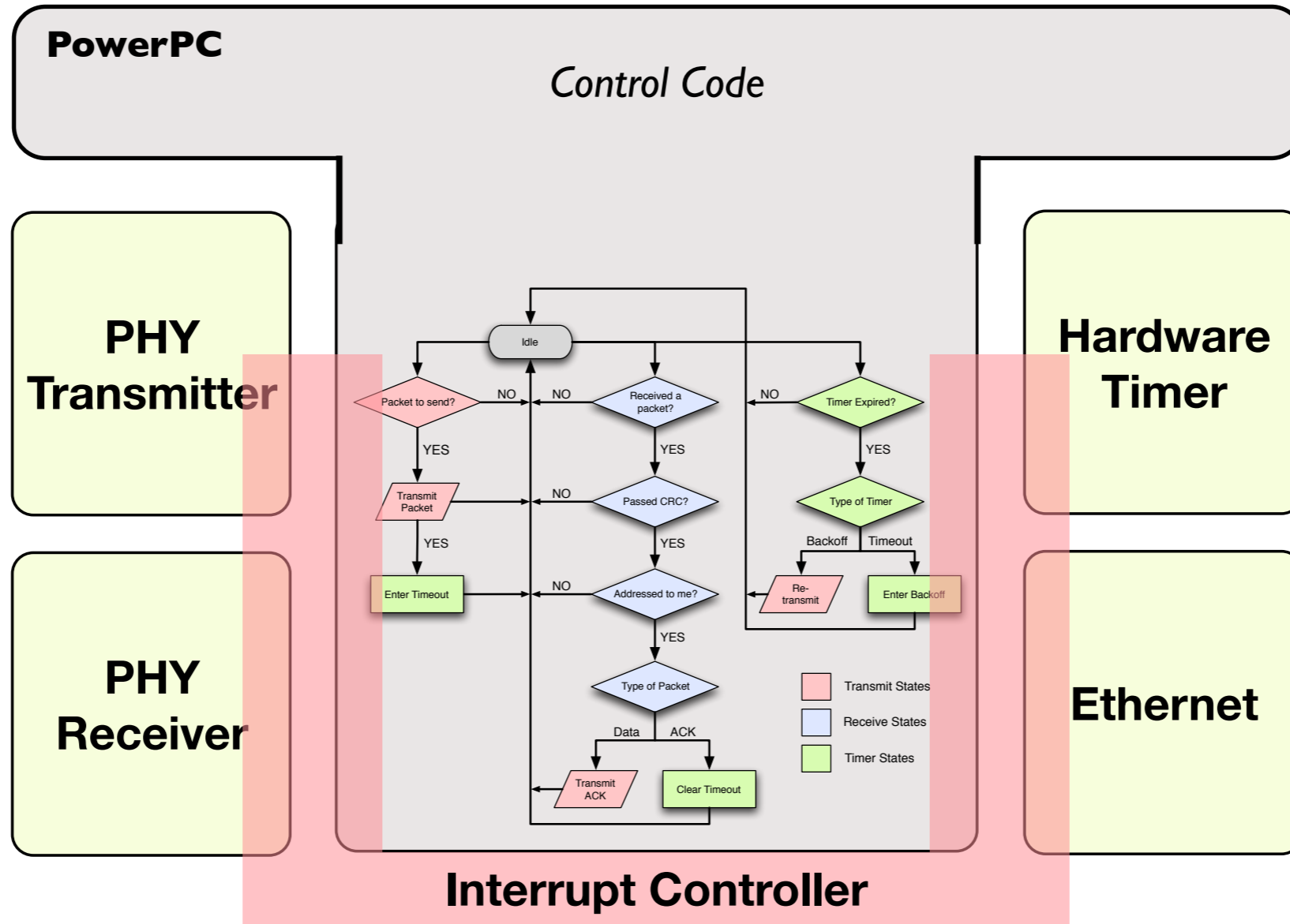
RS232
Serial

Hardware Platform

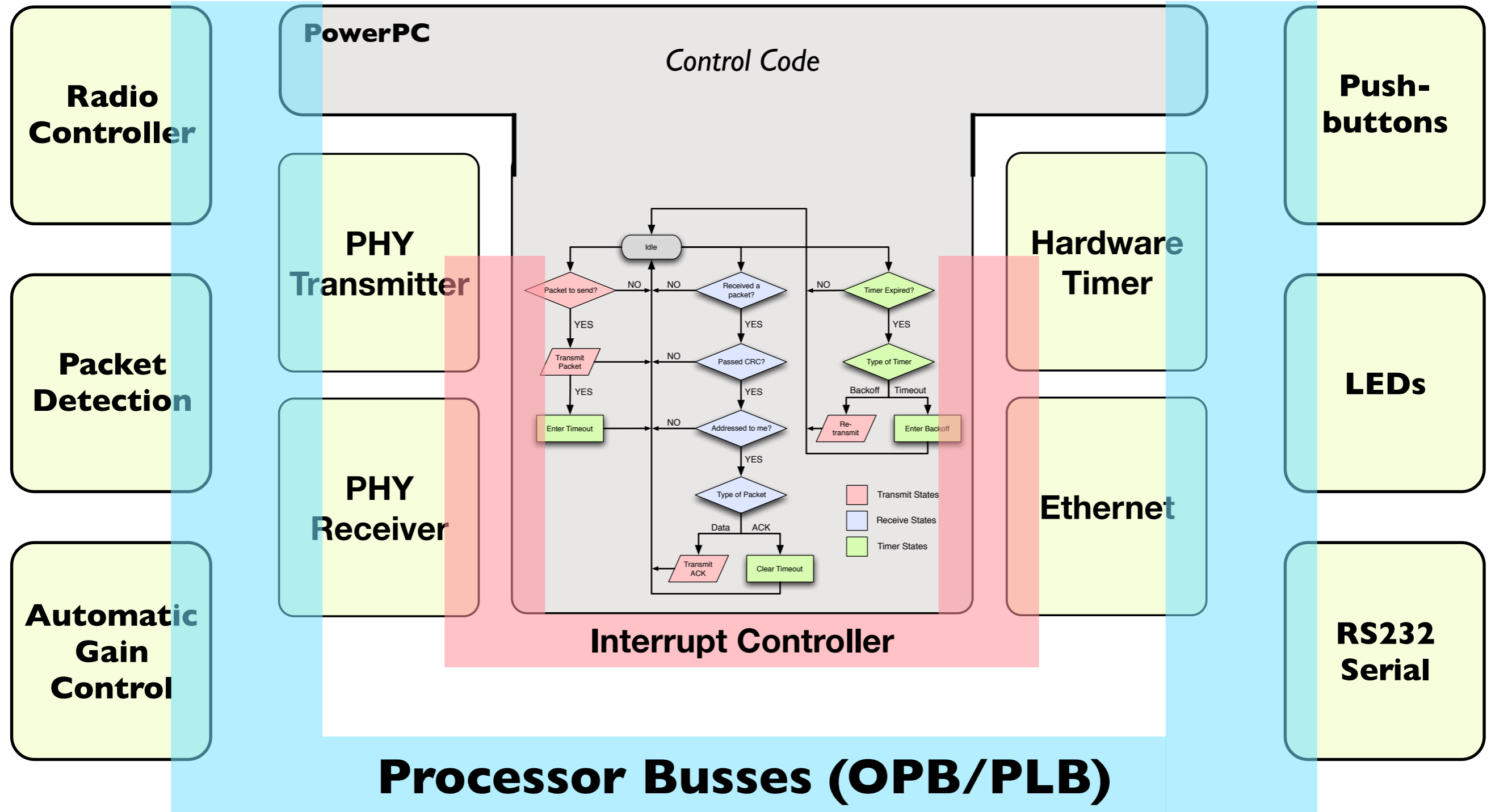
Radio Controller

Packet Detection

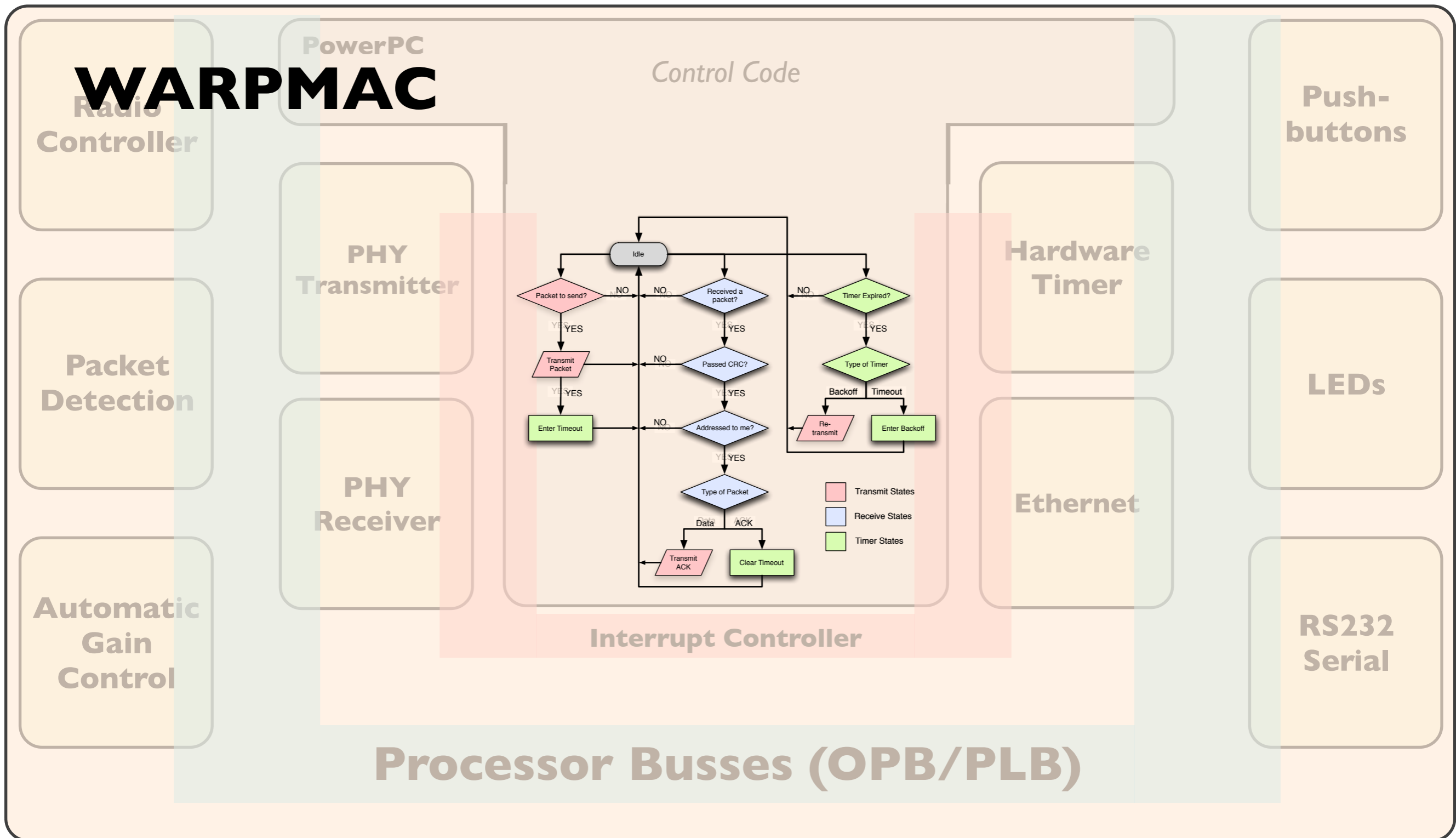
Automatic Gain Control



Hardware Platform



Hardware Platform



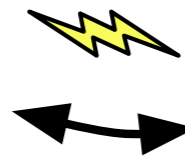
Detailed Example

CSMA

Node 0



Node 1



Experimental Wireless

Ethernet

WARPMAC Structs

Macframe:

phyHeader header */* Another struct */*

unsigned char isNew */* Flag for new packets */*

WARPMAC Structs

phyHeader:

```
unsigned char fullRate; /* Payload modulation rate */
unsigned char reserved4; /* Unused */
unsigned short int length; /* Payload length */
unsigned char pktType; /* Packet type */
unsigned char destAddr[6]; /* Destination address */
unsigned char srcAddr[6]; /* Source address */
unsigned char currReSend; /* Re-send count */
unsigned char reserved1; /* Unused */
unsigned char reserved2; /* Unused */
unsigned char reserved3; /* Unused */
unsigned short int checksum; /* CRC placeholder */
```

Fully documented in API (http://warp.rice.edu/WARP_API)

```

int main(){
    unsigned char myId[6] = 0;

    //Read Dip Switch value from FPGA board
    //This value will be used as an index into the routing table for other nodes
    myID = warpnet_getMyID();

    //Create an arbitrary address for this node
    unsigned char myAddr[6] = {0x16,0x24,0x63,0x53,0xe2,0xc2+myID};

    memcpy(myAddr, myAddr, 6);

    //Fill an arbitrary routing table so that nodes know each others' addresses
    unsigned char i;
    for(i=0; i<16; i++){
        routeTable[i].addr[0] = myAddr[0];
        routeTable[i].addr[1] = myAddr[1];
        routeTable[i].addr[2] = myAddr[2];
        routeTable[i].addr[3] = myAddr[3];
        routeTable[i].addr[4] = myAddr[4];
        routeTable[i].addr[5] = myAddr[5]+i-myID;
    }

    //Initialize the framework
    warpnet_init();

    warpnet_setMacAddr(myAddr);

    warpnet_setMaxResend(8);
    warpnet_setMaxCW(1);
    warpnet_setT(0.001);
    warpnet_setSTofTime(3);

    warpnet_setRxBuffer(&rxBuffer, 8);
    warpnet_setTxBuffer(1);

    memcpy(txBuffer_header.srcAddr, myAddr, 6);

    warpnet_setGoodPacketHandler(receiveGoodPacket);
    warpnet_setBadPacketHandler(receiveBadPacket);
    warpnet_setTimerHandler(timerExpire);
    warpnet_setEventHandler(ethernet_callback);

    warpnet_setChannel(CAN2, 0);

```

```

int main(){
    unsigned char myId[6] = 0;

    //Read Dip Switch value from FPGA board
    //This value will be used as an index into the routing table for other nodes
    myID = warpmac_getMyID();

    //Create an arbitrary address for this node
    unsigned char myAddr[6] = {0x16,0x24,0x63,0x53,0xe2,0xc2+myID};

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        routeTable[i].addr[4] = myAddr[4];
        routeTable[i].addr[5] = myAddr[5]+i-myID;
    }

    //Initialize the framework
    warpmac_init();

    warpmac_setMacAddr(myAddr);

    warpmac_setMaxResend(8);
    warpmac_setMaxCW(1);
    warpmac_setTimeout(160);
    warpmac_setSTime(8);

    warpmac_setRxBuffer(&rxBuffer, 8);
    warpmac_setTxBuffer(1);

    memcpy(txBuffer_header.srcAddr, myAddr, 6);

    warpmac_setGoodPacketHandler(receiveGoodPacket);
    warpmac_setBadPacketHandler(receiveBadPacket);
    warpmac_setTimerHandler(timerExpire);
    warpmac_setEventHandler(ethernet_callback);

    warpmac_setChannel(CHEF_2, 8);

```

- Reads the value from the dip switch on the FPGA board for use as identification
- This function also displays the value on the seven-segment displays

```

int main(){
    unsigned char myId[6];

    //Read Dlg Switch value from FPGA board
    //This value will be used as an Index into the routing table for other nodes
    myID = warpmac_getMyID();

    //Create an arbitrary address for this node
    unsigned char myAddr[6] = {0x16,0x24,0x63,0x53,0x02,0xc2+myID};

    memcpy(myAddr, myAddr, 6);

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        routeTable[i].addr[5] = myAddr[5]+i-myID;
    }

    //Initialize the framework
    warpmac_init();

    warpmac_setMacAddr(myAddr);

    warpmac_setMaxRsend(8);
    warpmac_setMaxCv(1);
    warpmac_setTimeout(100);
    warpmac_setSTotTime(5);

    warpmac_setRxBuffer(&rxBuffer, 8);
    warpmac_setTxBuffer(1);

    memcpy(txBuffer_header.srcAddr, myAddr, 6);

    warpmac_setGoodPacketHandler(receiveGoodPacket);
    warpmac_setBadPacketHandler(receiveBadPacket);
    warpmac_setTimerHandler(timerExpire);
    warpmac_setEmochandler(ethernet_callback);

    warpmac_setChannel(CHE_2, 8);

```

- Defines an arbitrary address, based on the node ID
- Specifies a crude “routing table” to allow nodes to communicate with one another using only the node IDs

```

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    warpmac_setSlotTime(9);

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    warpmac_setTxBuffer(1);

    memcpy(txBuffer_header.srcAddr, myAddr, 6);

    warpmac_setGoodPacketHandler(receiveGoodPacket);
    warpmac_setBadPacketHandler(receiveBadPacket);
    warpmac_setTimerHandler(timerExpire);
    warpmac_setEventHandler(ethernet_callback);

    warpmac_setChannel(CAN_2, 0);

```

- Initializes the framework
- Initializes PHY, radio, AGC, packet detection, interrupts, etc.
- Sets specific parameters
 - 8 resends
 - Maximum contention window of $5 * (\text{Slot-time})$
 - 160 usec timeout
 - 9 usec Slot-time

```

int main(){
    unsigned char myId[6] = 0;

    //Read Dlg Switch value from FPGA board
    //This value will be used as an Index into the routing table for other nodes
    myID = warpnet_getMyID();

    //Create an arbitrary address for this node
    unsigned char myAddr[6] = {0x16,0x24,0x63,0x53,0xe2,0xc2+myID};

    memcpy(myAddr, myAddr, 6);

    //Fill an arbitrary routing table so that nodes know each others' addresses
    unsigned char i;
    for(i=0; i<16; i++){
        routeTable[i].addr[0] = myAddr[0];
        routeTable[i].addr[1] = myAddr[1];
        routeTable[i].addr[2] = myAddr[2];
        routeTable[i].addr[3] = myAddr[3];
        routeTable[i].addr[4] = myAddr[4];
        routeTable[i].addr[5] = myAddr[5]+i-myID;
    }

    //Initialize the framework
    warpnet_init();

    warpnet_setMacAddr(&myAddr);

    warpnet_setMaxResend(8);
    warpnet_setMaxCW(1);
    warpnet_setT(0.001);
    warpnet_setLSInterval(3);

    warpnet_setRxBuffer(&rxBuffer, 8);
    warpnet_setTxBuffer(1);

    memcpy(txBuffer.header.srcAddr, myAddr, 6);

    warpnet_setGoodPacketHandler(receiveGoodPacket);
    warpnet_setBadPacketHandler(receiveBadPacket);
    warpnet_setTimerHandler(timerExpire);
    warpnet_setEventHandler(ethernet_callback);

    warpnet_setChannel(CHE_2, 8);

```



```

memcpy(myAddr, myAddr, 6);

//Fill an arbitrary routing table so that nodes know each others' addresses
unsigned char i;
for(i=0; i<10; i++){
    routeTable[i].addr[0] = myAddr[0];
    routeTable[i].addr[1] = myAddr[1];
    routeTable[i].addr[2] = myAddr[2];
    routeTable[i].addr[3] = myAddr[3];
    routeTable[i].addr[4] = myAddr[4];
    routeTable[i].addr[5] = myAddr[5]+i-myID;
}

//Initialize the framework
warpmac_init();

warpmac_setMacAddr(&myAddr);

warpmac_setMaxResend(8);
warpmac_setMaxCR(3);
warpmac_setTimeout(100);
warpmac_setSLofTime(5);

warpmac_setRxBuffer(&rxBuffer, 8);
warpmac_setTxBuffer(1);

memcpy(txBuffer_header.srcAddr, myAddr, 6);

warpmac_setGoodPacketHandler(receiveGoodPacket);
warpmac_setBadPacketHandler(receiveBadPacket);
warpmac_setTimerHandler(timerExpire);
warpmac_setEventHandler(ethernet_callback);

warpmac_setChannel(CH2, 0);

warpmac_enableCSMA();
warpmac_enableEthernetInterrupt();

//Set the modulation scheme use for base rate (header) symbols
warpmac_setBaseRate(OPSK);

while(1){
}

```

```

memcpy(myAddr, myAddr, 6);

//Fill an arbitrary routing table so that nodes know each others' addresses
unsigned char i;
for(i=0; i<10; i++){
    routeTable[i].addr[0] = myAddr[0];
    routeTable[i].addr[1] = myAddr[1];
    routeTable[i].addr[2] = myAddr[2];
    routeTable[i].addr[3] = myAddr[3];
    routeTable[i].addr[4] = myAddr[4];
    routeTable[i].addr[5] = myAddr[5]+i-myID;
}

//Initialize the framework
warpmac_init();

warpmac_setMacAddr(myAddr);

warpmac_setMaxRsend(8);
warpmac_setMaxCw(1);
warpmac_setTimeout(100);
warpmac_setSlotTime(9);

warpmac_setRxBuffer(&rxBuffer, 80);
warpmac_setTxBuffer(1);

memcpy(txBuffer_header.srcAddr, myAddr, 6);

warpmac_setGoodPacketHandler(receiveGoodPacket);
warpmac_setBadPacketHandler(receiveBadPacket);
warpmac_setTimerHandler(timerExpire);
warpmac_setEventHandler(ethernet_callback);

warpmac_setChannel(CH2, 0);

warpmac_enableCSMA();
warpmac_enableEthernetInterrupt();

//Set the modulation scheme use for base rate (header) symbols
warpmac_setBaseRate(OPSK);

while(1){
}

```

- Tells WARPMAC to receive wireless packets into a particular buffer
- Tells WARPMAC to send wireless packets from a particular buffer
- Registers user interrupt handlers with the frameworks

```

memcpy(myAddr, myAddr, 6);

//Fill an arbitrary routing table so that nodes know each others' addresses
unsigned char i;
for(i=0; i<10; i++){
    routeTable[i].addr[0] = myAddr[0];
    routeTable[i].addr[1] = myAddr[1];
    routeTable[i].addr[2] = myAddr[2];
    routeTable[i].addr[3] = myAddr[3];
    routeTable[i].addr[4] = myAddr[4];
    routeTable[i].addr[5] = myAddr[5]+i-myID;
}

//Initialize the framework
warpnet_init();

warpnet_setMacAddr(myAddr);

warpnet_setMaxResend(8);
warpnet_setMaxCW(1);
warpnet_setTimeout(100);
warpnet_setSlotTime(9);

warpnet_setRxBuffer(&rxBuffer, 8);
warpnet_setTxBuffer(1);

memcpy(txBuffer_header.srcAddr, myAddr, 6);

warpnet_setGoodPacketHandler(receiveGoodPacket);
warpnet_setBadPacketHandler(receiveBadPacket);
warpnet_setTimerHandler(timerExpire);
warpnet_setEventHandler(ethernet_callback);

warpnet_setChannel(CH2, 8);

warpnet_enableCSMA();
warpnet_enableEthernetInterrupt();

//Set the modulation scheme use for base rate (header) symbols
warpnet_setBaseRate(OPSK);

while(1){
}

```

```

    macTable[1], addr[1], myAddr[1]+1-myID);
}

//Initialize the framework
warpmac_init();

warpmac_setMacAddr(&myAddr);

warpmac_setMaxResend(8);
warpmac_setMaxCR(1);
warpmac_setTimeout(160);
warpmac_setSTotTime(3);

warpmac_setRxBuffer(&rxBuffer, 8);
warpmac_setTxBuffer(1);

memcpy(txBuffer_header.srcAddr, myAddr, 6);

warpmac_setGoodPacketHandler(receiveGoodPacket);
warpmac_setBadPacketHandler(receiveBadPacket);
warpmac_setTimerHandler(timerExpire);
warpmac_setLmacHandler(ethernet_callback);

warpmac_setChannel(CH2_2, 8);

warpmac_enableCSMA();
warpmac_enableEthernetInterrupt();

//Set the modulation scheme use for base rate (header) symbols
warpmac_setBaseRate(OPSK);

while(1){
}

return;
}

```

```

    }

    //Initialize the framework
    warpmac_init();

    warpmac_setMacAddr(&myAddr);

    warpmac_setMaxRsendCF();
    warpmac_setMaxCR(1);
    warpmac_setTimeout(100);
    warpmac_setSTotTime(0);

    warpmac_setRxBuffer(&rxBuffer, 8);
    warpmac_setTxBuffer(1);

    memcpy(txBuffer_header.srcAddr, myAddr, 6);

    warpmac_setGoodPacketHandler(receiveGoodPacket);
    warpmac_setBadPacketHandler(receiveBadPacket);
    warpmac_setTimerHandler(timerExpire);
    warpmac_setEventHandler(ethernet_callback);

    warpmac_setChannel(CH2, 8);

    warpmac_enableCSMA();
    warpmac_enableEthernetInterrupt();

    //Set the modulation scheme use for base rate (header) symbols
    warpmac_setBaseRate(QPSK);

    while(1)
    {
        return;
    }
}

```

- Sets the frequency band to 802.11 channel 8 of the 2.4GHz band
- Enable carrier-sensing mode of WARPMAC
- Enable the Ethernet interrupt
- Set the base modulation rate to QPSK (must be agreed upon by all nodes in the network)
- Spins forever in a while loop, waiting for an interrupt

Case 1:

Packet received from Ethernet

```

int ethernet_callback(Xuint32 length, char* payload){
    warpmac_disableEthernetInterrupt();
    txBuffer.header.currReSend = 0;
    txBuffer.isNew = 1;
    txBuffer.header.length = length;
    txBuffer.header.pktType = DATAPACKET;

    //Set the modulation scheme for the packet's full-rate symbols
    txBuffer.header.fullRate = QPSK;

    //Copy in the packet's destination MAC address
    //Hard-coded as this node's partner node
    memcpy(txBuffer.header.destAddr, routeTable[(myID+1)%2].addr, 6);

    if(warpmac_carrierSense()){
        warpmac_sendOfdm(&txBuffer);
        warpmac_setTimer(TIMEOUT);
    }
    else{
        warpmac_setTimer(BACKOFF);
    }
    return 0;
}

```

- Disables the Ethernet interrupt line until this frame is dealt with
- Metadata and header information is filled in
 - isNew = 1, since it is a new packet
 - Length, packet type, full rate modulation order and the destination MAC address are filled into the header
- If the medium is free, the packet is sent and a timeout begins

Case 2:

*“Bad” packet received from
OFDM*


```
int receiveBadPacket(Macframe* packet) {  
    warpmac_incrementLEDLow();  
}
```

- If we receive a packet that fails checksum
- Blink the bottom LEDs
- This way we can have a visualization of channel quality

Case 3:

***“Good” data packet received
from OFDM***

```

int receiveGoodPacket(Macframe* packet) {

    warpmac_incrementLEDHigh();

    if(warpmac_addressedToMe(packet)){
        Macframe ackPacket;
        switch(packet->header.pktType){
            case ACKPACKET:

                if(warpmac_inTimeout()){
                    warpmac_clearTimer(TIMEOUT);
                    txBuffer.header.currReSend = 0;
                    txBuffer.isNew = 0;
                    warpmac_enableEthernetInterrupt();
                }

                break;
            case DATAPACKET:
                warpmac_leftHex(packet->header.currReSend);
                ackPacket.header.length = 0;
                ackPacket.header.pktType = ACKPACKET;
                ackPacket.header.fullRate = QPSK;
                memcpy(ackPacket.header.srcAddr, myAddr, 6);
                memcpy(ackPacket.header.destAddr, packet->header.srcAddr, 6);
                warpmac_setTxBuffer(2);
                warpmac_sendOfdm(&ackPacket);
                warpmac_setTxBuffer(1);

                packet->header.currReSend = 0;
                packet->isNew = 1;
                warpmac_phyInterruptClear();
                warpmac_sendEthernet(packet);
                packet->header.currReSend = 0;
                packet->isNew = 0;

                break;
        }
    }
    return 0;
}

```

- Blink the top LEDs
- If destination address is equal to my source address and the type is a data packet
- Create an acknowledgment and send it
- Send the packet over Ethernet

Case 4:

*“Good” acknowledgment
packet received from OFDM*

```

int receiveGoodPacket(Macframe* packet) {

    warpmac_incrementLEDHigh();

    if(warpmac_addressedToMe(packet)){
        Macframe ackPacket;
        switch(packet->header.pktType){
            case ACKPACKET:

                if(warpmac_inTimeout()){
                    warpmac_clearTimer(TIMEOUT);
                    txBuffer.header.currReSend = 0;
                    txBuffer.isNew = 0;
                    warpmac_enableEthernetInterrupt();
                }

                break;
            case DATAPACKET:
                warpmac_leftHex(packet->header.currReSend);
                ackPacket.header.length = 0;
                ackPacket.header.pktType = ACKPACKET;
                ackPacket.header.fullRate = QPSK;
                memcpy(ackPacket.header.srcAddr, myAddr, 6);
                memcpy(ackPacket.header.destAddr, packet->header.srcAddr, 6);
                warpmac_setTxBuffer(2);
                warpmac_sendOfdm(&ackPacket);
                warpmac_setTxBuffer(1);

                packet->header.currReSend = 0;
                packet->isNew = 1;
                warpmac_phyInterruptClear();
                warpmac_sendEthernet(packet);
                packet->header.currReSend = 0;
                packet->isNew = 0;

                break;
        }
    }
    return 0;
}

```

- Blink the top LEDs
- If destination address is equal to my source address and the type is an acknowledgment
- If a timeout is currently running (i.e., the node is waiting on an ACK)
 - Stop the timer
 - Turn Ethernet interrupts back on (they were disabled in the ethernet handler)

Case 5:

Timeout timer expires

```

int timerExpire(unsigned char timerType){
    int status;
    switch(timerType){
        case TIMEOUT:
            if(txBuffer.isNew){
                status = warpmac_incrementResend(&txBuffer);
                if(status == 0){
                    warpmac_enableEthernetInterrupt();
                    return 0;
                }
                warpmac_setTimer(BACKOFF);
                return 0;
            }
            break;
        case BACKOFF:
            if(warpmac_carrierSense()){
                warpmac_sendOfdm(&txBuffer);
                warpmac_setTimer(TIMEOUT);
            }
            else{
                warpmac_setTimer(BACKOFF);
            }
            break;
        return 0;
    }
}

```

- Increment the resend field of the packet
- Enter a backoff
- Re-enable Ethernet interrupts if maximum retransmissions were met

Case 6:

Backoff timer expires


```

int timerExpire(unsigned char timerType){
    int status;
    switch(timerType){
        case TIMEOUT:
            if(txBuffer.isNew){
                status = warpmac_incrementResend(&txBuffer);
                if(status == 0){
                    warpmac_enableEthernetInterrupt();
                    return 0;
                }
                warpmac_setTimer(BACKOFF);
                return 0;
            }
            break;
        case BACKOFF:
            if(warpmac_carrierSense()){
                warpmac_sendOfdm(&txBuffer);
                warpmac_setTimer(TIMEOUT);
            }
            else{
                warpmac_setTimer(BACKOFF);
            }
            break;
        return 0;
    }
}

```

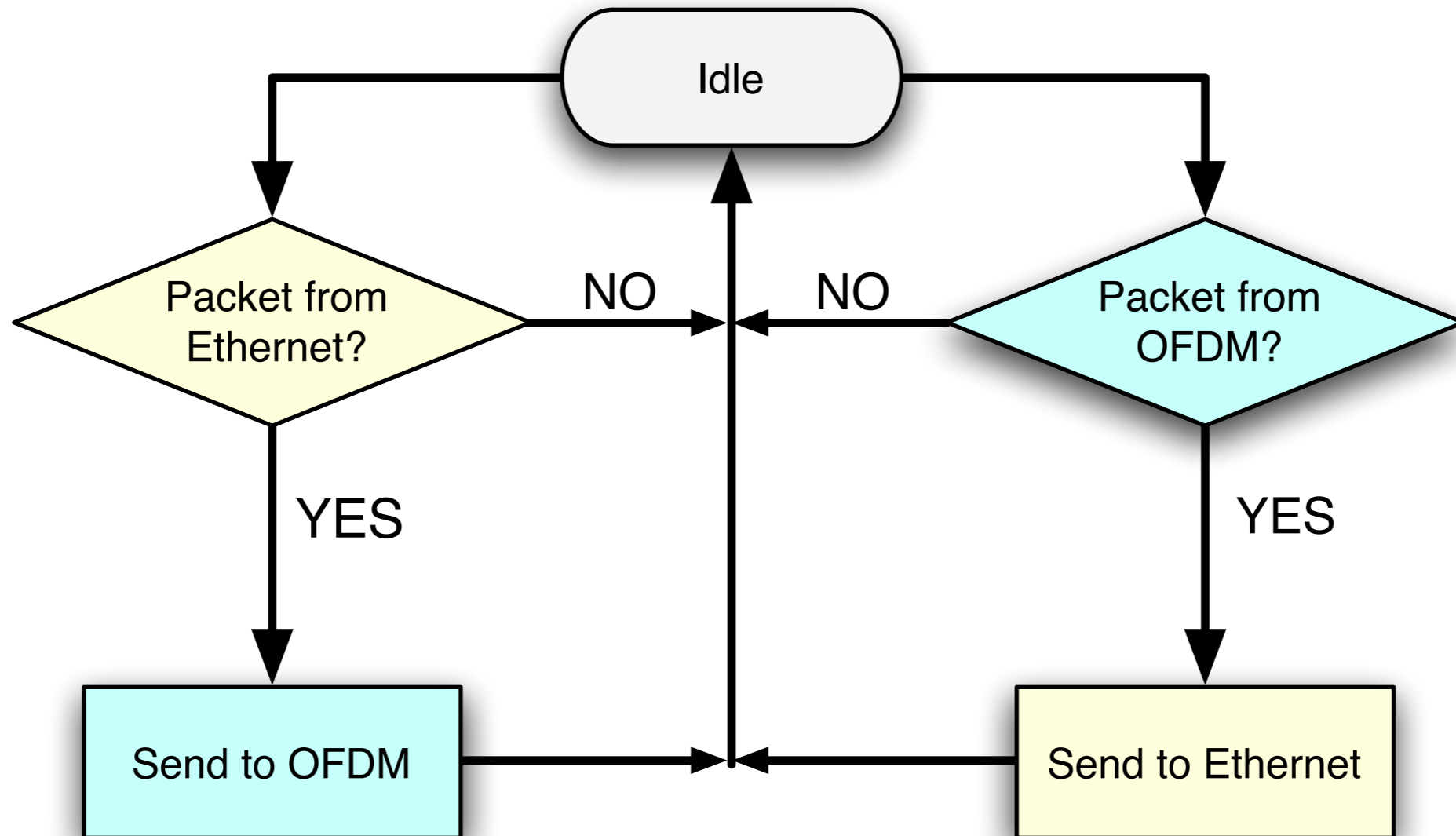
- If the medium is free
- Send it over OFDM
- Enter a timeout
- Otherwise, start another timeout

Questions?

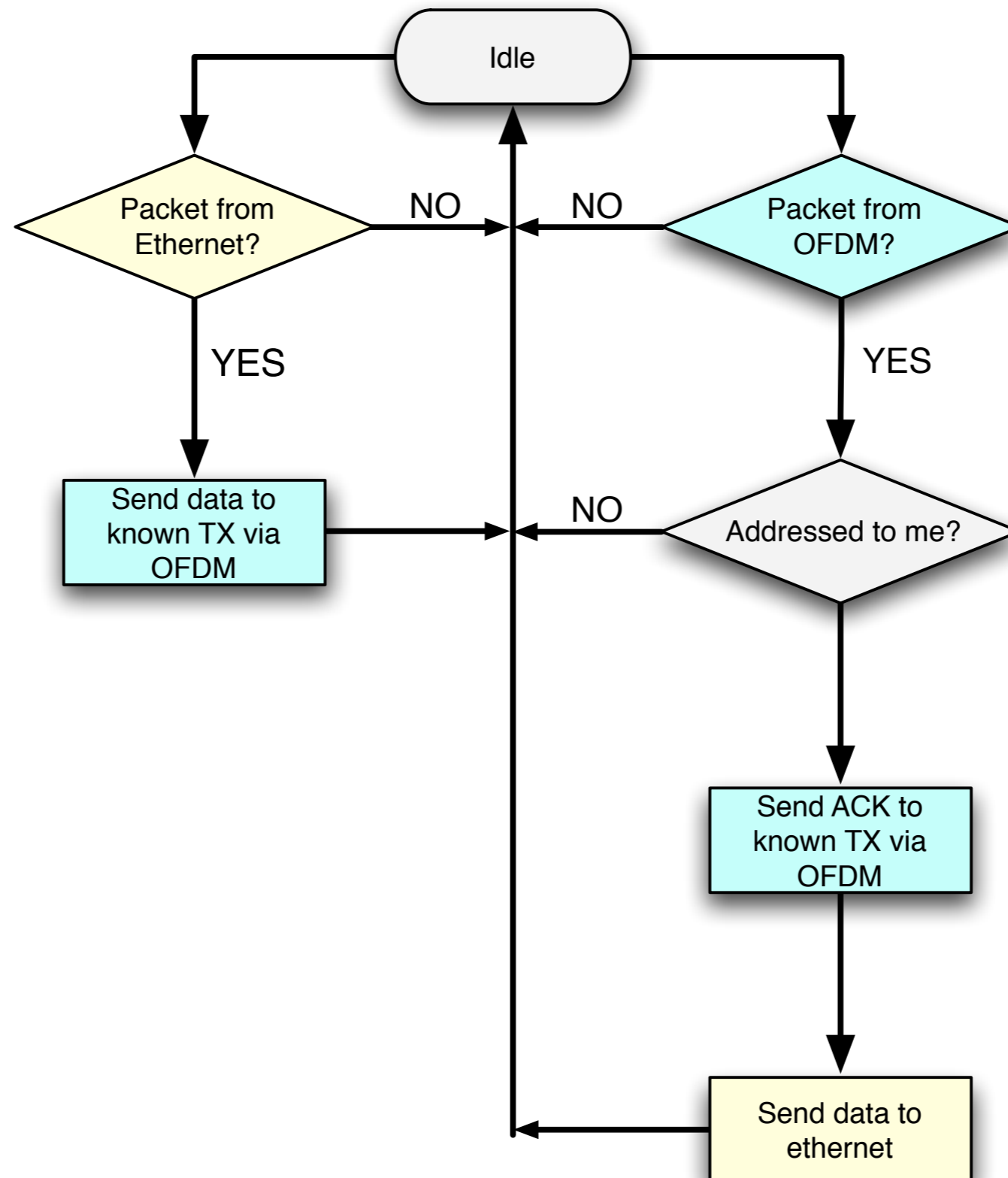
Lab Exercises

- Lab 4 - noMAC: Simple “MAC” layer
- Lab 5 - uniMAC: Unidirectional MAC
- Lab 6 - hopMAC: Channel-hopping MAC

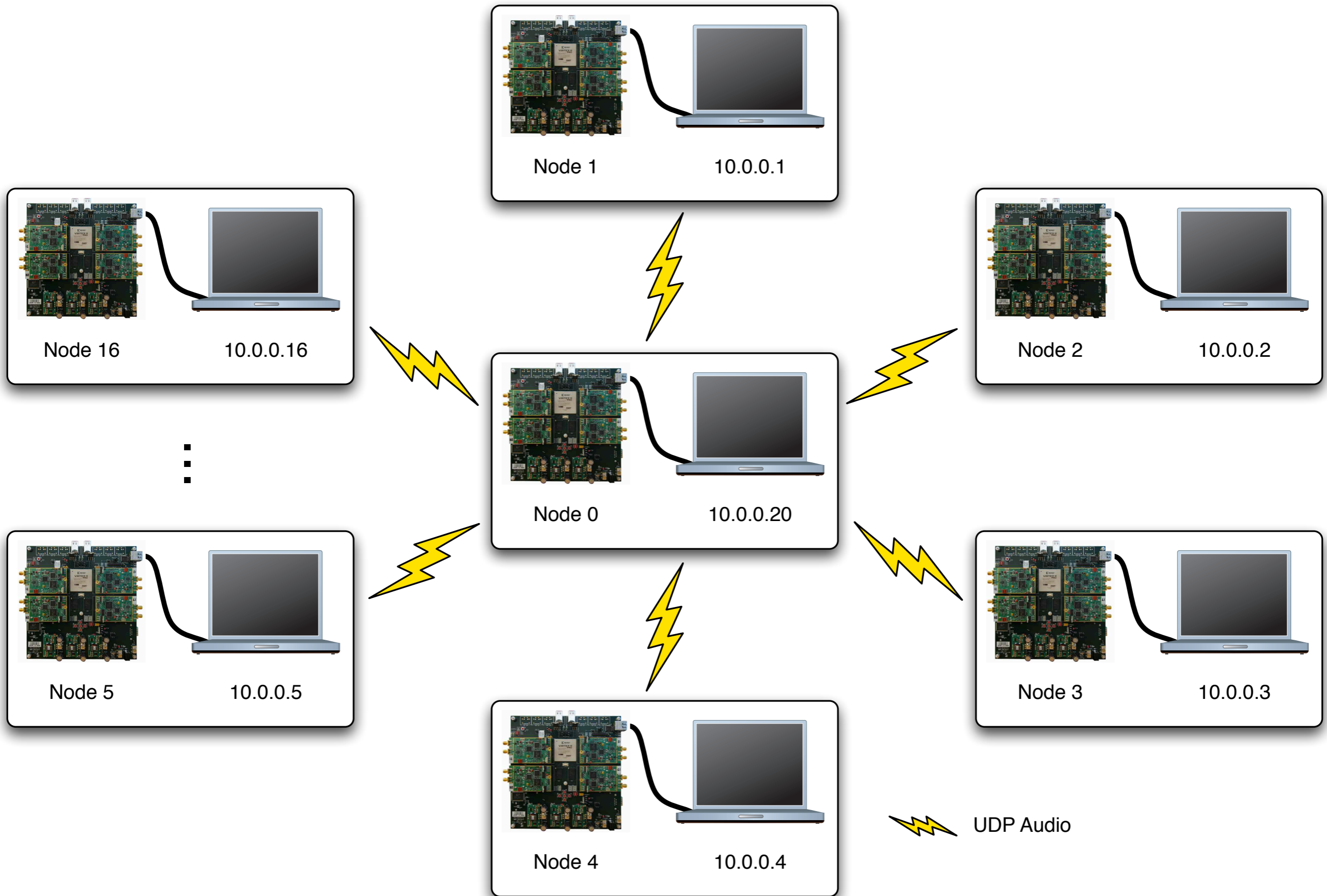
noMac



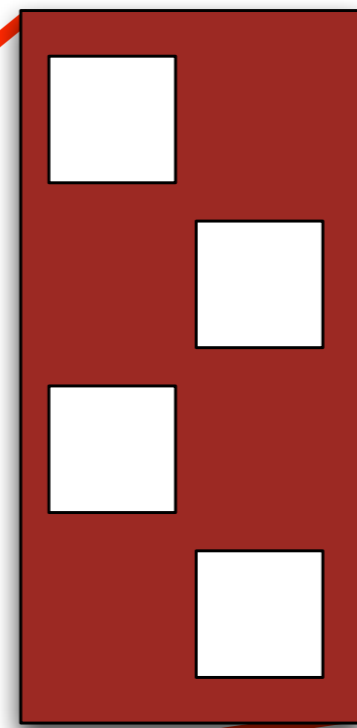
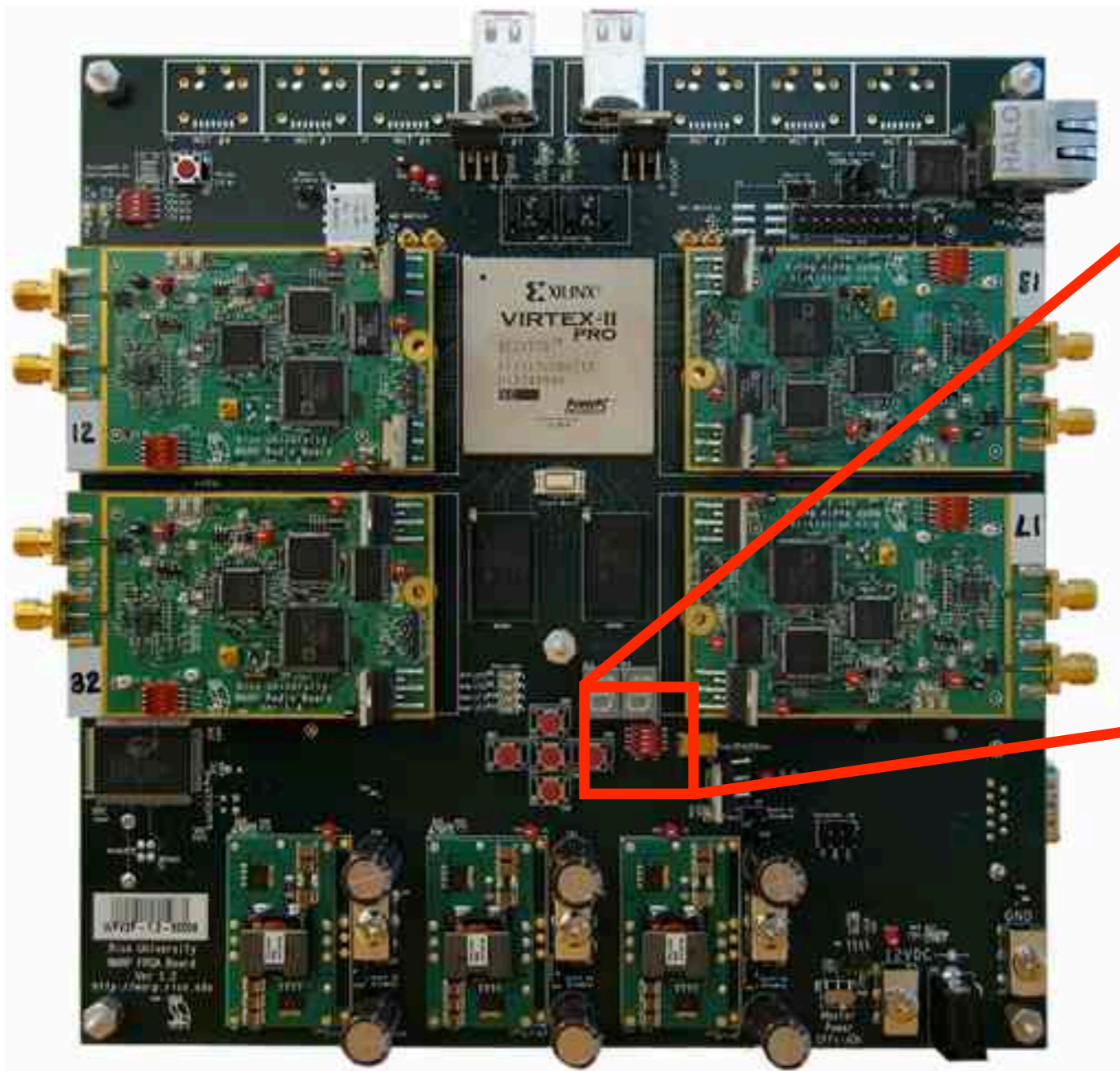
uniMac



uniMac



uniMac Lab

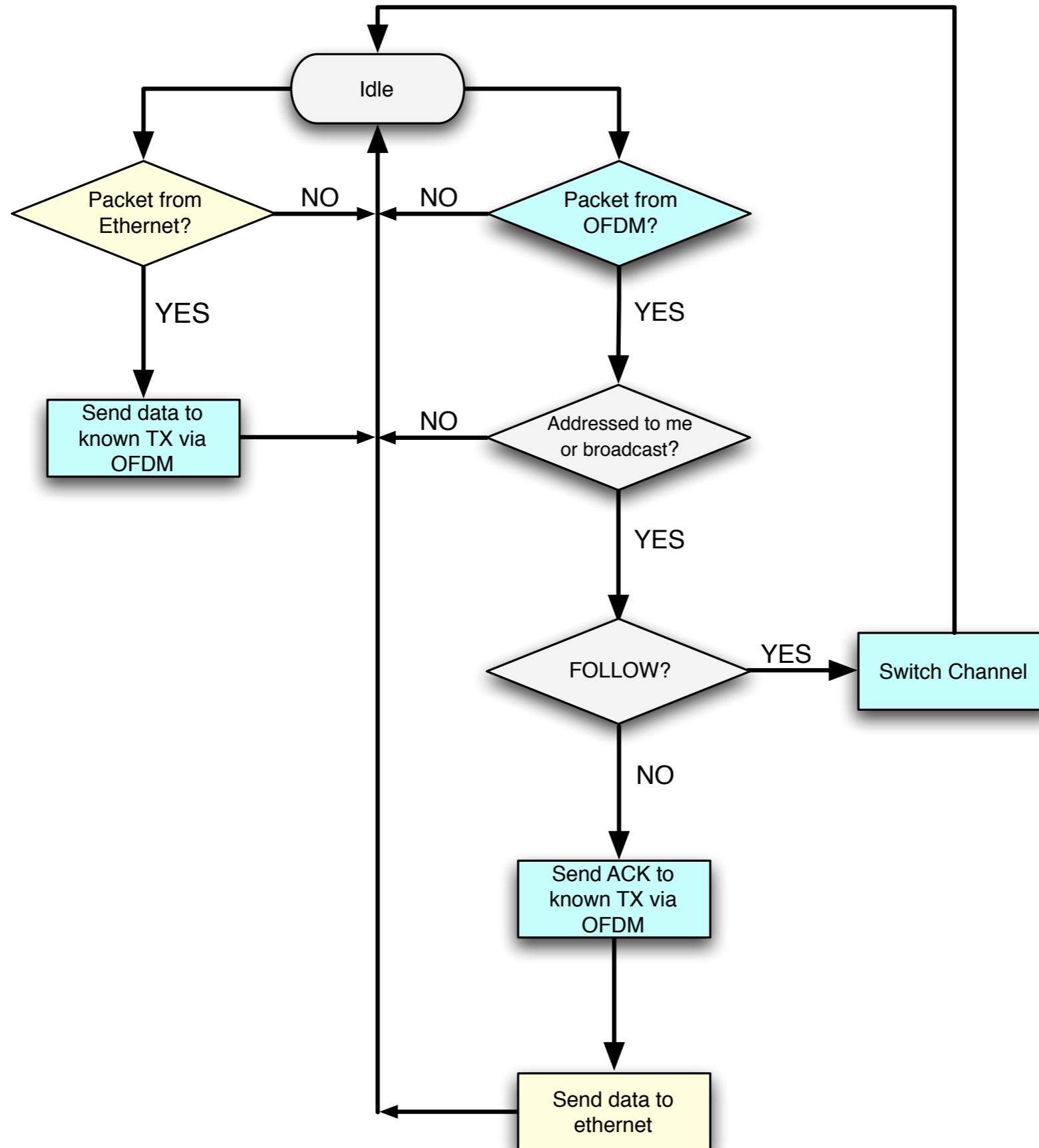


Most Significant Bit (MSB)

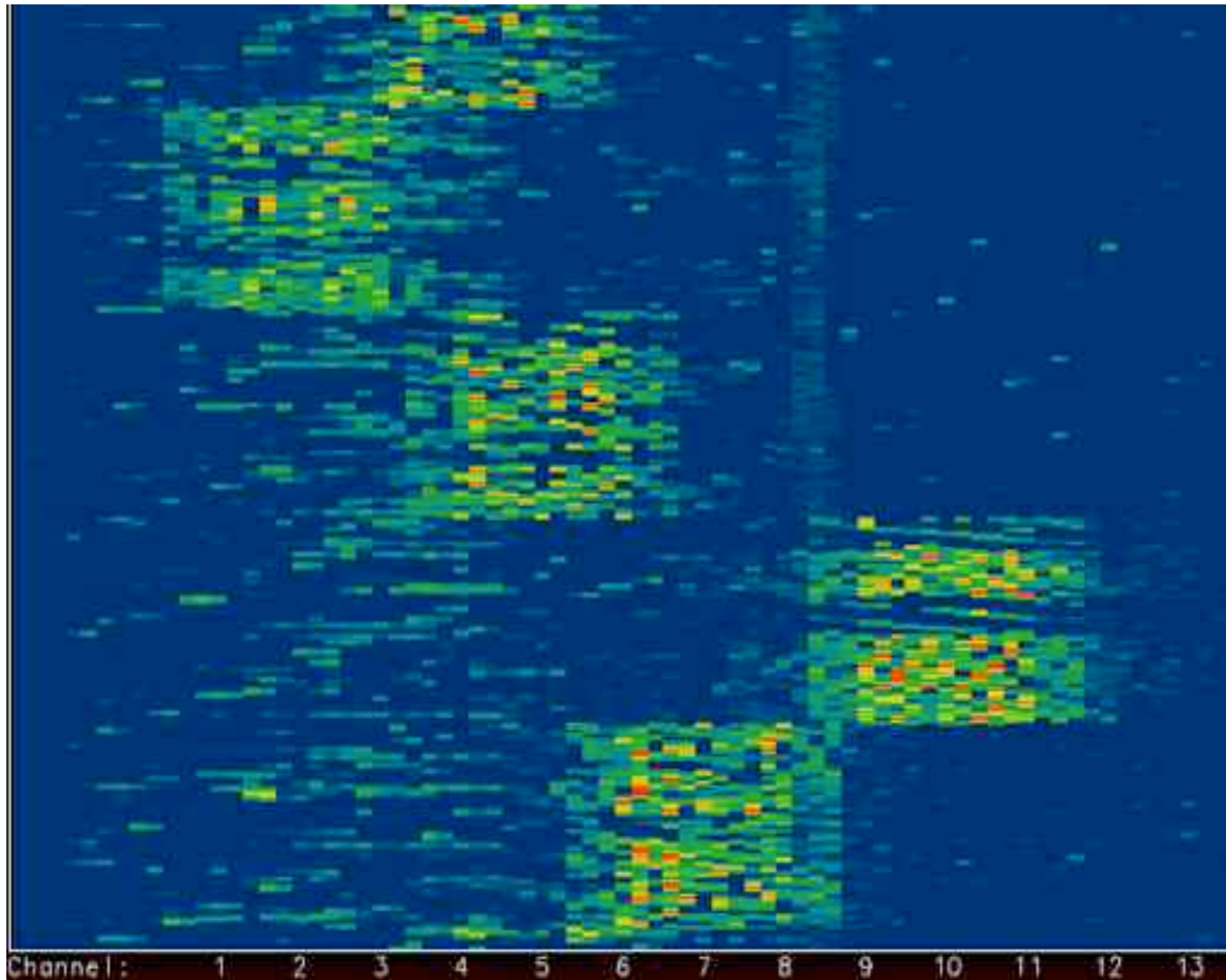
Least Significant Bit (LSB)

Node 5

hopMac



hopMac Lab



Questions?

Remember to use the API:

http://warp.rice.edu/WARP_API

Logistics

- Review forms
- Contacting us
 - Support & technical questions
 - <http://warp.rice.edu/forums/>
 - Hardware sales
 - <http://warp.rice.edu/>
 - warp-project@rice.edu