WARP WIRELESS OPEN-ACCESS RESEARCH PLATFORM

Lab 4: noMac

Chris Hunter & Patrick Murphy

Rice University WARP Project

Document Revision 11

November 13, 2008

1 Introduction

The WARP networking lab is intended to introduce the tools required for MAC development on WARP. In previous labs, code on the PowerPC was used to drive cores running in the FPGA fabric. We will make extensive use of the WARPMAC framework (http://warp.rice.edu/WARP_API). The framework handles the hardware interaction, and we will program a basic MAC.

In this particular lab, we will implement the simplest MAC: no MAC at all. We will use the functions of WARPMAC to create a true bridge between Ethernet and the wireless PHY. Any packet that is received on one medium will be forwarded to the other medium as-is; there will be no addressing and no retransmissions. This behavior is shown in Figure 1.

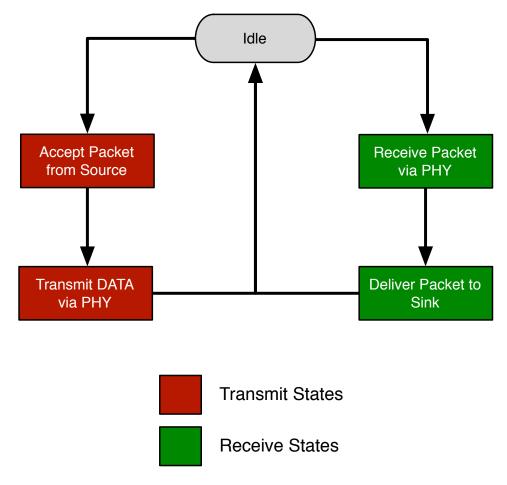


Figure 1: Ethernet MAC - Wireless PHY Hub algorithm

The WARP API will be required throughout this lab exercise. The full API documentation is available online (http://warp.rice.edu/WARP_API). Skeleton code is provided which compiles without any modification. This code monitors the PHY for good and bad packets. It blinks LEDs on the FPGA board to indicate reception of both. Within the skeleton code are comments describing the code that needs to be written.

Note: All files are stored in C:\workshop\userN\ where userN is your user login location. This location will be referred to as .\ for the rest of the lab.

2 Instructions

- 1. Open the *system.xmp* in .\Labs4_6_MAC\
- 2. Because we will only be dealing with the software project in this lab, we can ignore everything in the "System Assembly View." Click on the "Applications" tab as shown in Figure 2

	Xilinx Platform Studi	o - /home/chunter/EDK/WOR	KSHOP/Labs_4_6_MAC/syst	tem.xmp - [System Assembly]	View2]	
<u>File</u> <u>E</u> dit <u>V</u> iew <u>P</u> roject Ha <u>r</u> dware <u>S</u> ol	ftware Device Co <u>n</u> figu	ration <u>D</u> ebug Si <u>m</u> ulation <u>W</u> in	ndow <u>H</u> elp			_ 8 ×
] 🗋 🏓 🗐 🦆] 📅 🕅 ず] 🛤 🖉 🛛	- 🖻 🕼 🔟 🖻 🗗	😼 🖂 😺 🛛 🌬 🖉 🐼	1 🗠 🛓 🛐 🏤 🗍 👯 🏭 🧕	🛛 🔉 🛛 🔀 📰 🔄 🖉 🖉	, 🗄 🗉 🗁 🦎	
Project Applications IP Catalog	0 0 E	Bus Interfaces Ports	Addresses			SIF Filters
	<u>м м і</u>	Name	Bus Connection	IP Type	IP Version	<u> </u>
Software Projecte	⊳ <mark>⊳⊜⊜</mark> (ppc405	3.00.a	
Add Software Application Project		→ → ppc405_0_docm		dsocm_v10	2.00.b	
- Default: ppc405_0_bootloop	▋			isocm_v10	2.00.b	
Project: CSMAMAC		⊕ > plb0		plb_v46	1.03.a	
Project: NOMAC				plb_v46	1.03.a	
Processor: ppc405_0		₽ → ppc405_0_docm_cntlr		dsbram_if_cntlr	3.00.b	
Executable: /home/chunter/EDK/W				isbram_if_cntlr	3.00.b	
E Compiler Options	← ← ← ← ← ← ← ← ← ← ← ← ← ← ← ← ← ← ←			xps_bram_if_cntlr	1.00.a	
	(((((((((((((((((((xps_bram_if_cntlr	1.00.a	_
i⊞-Headers	ć	SRAM0_ZBT_512Kx32		xps_mch_emc	2.00.a	
Project: HOPMAC_Server	¢ i			xps_mch_emc	2.00.a	
Project: HOPMAC_Client		⊕ →dsocm_bram		bram_block	1.00.a	
Project: HALFMAC_Server	Þ Þ 🕂 🚽	🕂 🗢 isocm_bram		bram_block	1.00.a	
⊞ SProject: HALFMAC_Client			am	bram_block	1.00.a	
	▶ Þ		am	bram_block	1.00.a	
		₽ →eeprom_0		eeprom	1.06.a	
	÷			jtagppc_cntlr	2.01.b	
			0	ofdm_agc_mimo_plbw	1.01.a	
		Image: A state of the state		ofdm_pktdetector_mimo_plbw 1.00.a		
		🗭 🇢 ofdm timer olhw A		ofdm timer ollow	1.00 f	<u> </u>
↓	Block Diagram2 S	vstem Assembly View				
×		,,				<u>_</u>
Output Warning Error						1

Figure 2: Application Tab

- 3. Expand the **NOMAC** application. Within the "Sources" and "Headers" hierarchies, you will be greeted by a number of files required for the project to build:
 - *noMac.c* This file is the where all of the user's modifications will take place.
 - *warpmac.c* and *warpphy.c* These file contains all of the MAC development and PHY interface framework. These frameworks provide the user high-level functions for abstracting interactions with the wireless and wired network interfaces.
- 4. Open the *noMac.c* file and modify the skeleton code to implement the functionality described in the state machine above. There are helpful comments included in the skeleton code to guide your implementation.
- Choose Device Configuration → Update Bitstream to compile your code and create the FPGA programming file. Use iMPACT on your local PC to download the bitstream to your FPGA board to test your design.

3 Testing your MAC

If the MAC works as expected, the wireless network in the room should be equivalent to a wired Ethernet hub connecting every computer. To make sure this is the case, try this experiment:

• Use *ping* from the command-line on your local PC to see the effective round-trip times over the wireless network. A machine is available to respond to pings. The IP address of this machine will be posted in the lab.

Notice the performance of the link as a function of other groups completing the assignment. As more groups finish, the wireless medium will become more contention. The MAC we have built has no means of dealing with this contention, so performance will be quite poor. We will see the gains of a true MAC in the next lab.