



PowerPC™

System wide settings

Reference clock
frequency:

100.00 MHz

Processor clock
frequency:

200.00

MHz

Bus clock frequency:

50.00

MHz

Reset polarity:

Active HIGH

Processor configuration

Debug I/F

- ☒ FPGA JTAG
- ☐ CPU debug user pins only
- ☐ CPU debug and trace pins
- ☐ No debug



Cache setup

☐ Enable

On-chip memory (OCM)

(Use BRAM)

Data:

64 KB

Instruction:

128 KB

More Info

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