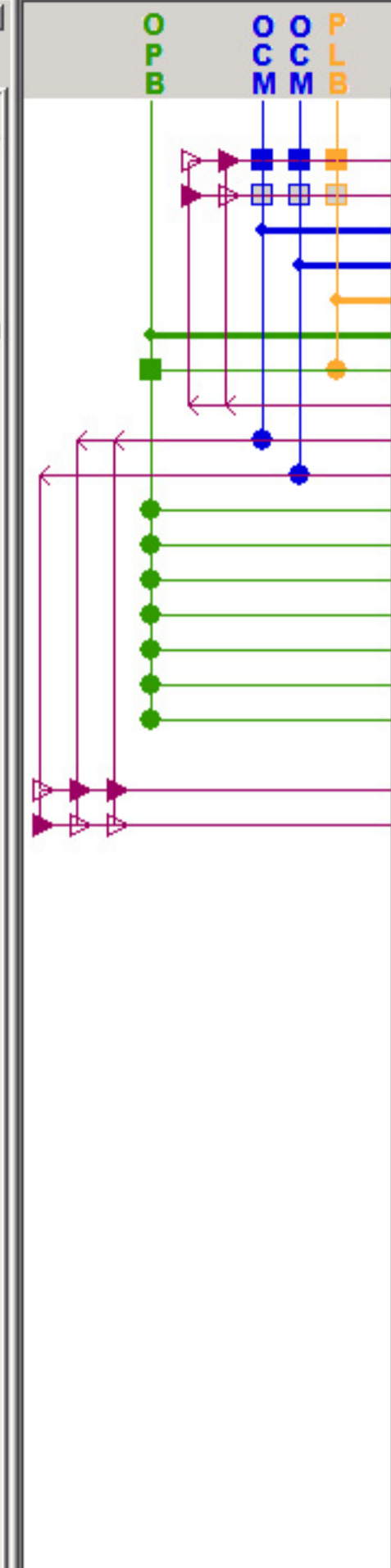


Project Applications IP Catalog

Platform

- Project Files
 - MHS File: system.mhs
 - MSS File: system.mss
 - UCF File: data/system.ucf
 - iMPACT Command File: etc/download.cmd
 - Implementation Options File: etc/fast_runtime.opl
 - Bitgen Options File: etc/bitgen.ut
- Project Options
 - Device: xc2vp70ff1517-6
 - Netlist: TopLevel
 - Implementation: XPS
 - HDL: VHDL
 - Sim Model: BEHAVIORAL
- Reference Files
 - Log Files
 - Report Files



Filters: Bus Interface Ports Addresses Connection Filters

Name	Bus Connection	Direction	IP Type	IP Version
ppc405_0			ppc405	2.00.c
ppc405_1			ppc405	2.00.c
iocm			isocm_v10	2.00.a
docm			dsocm_v10	2.00.a
plb			plb_v34	1.02.a
opb			opb_v20	1.10.c
plb2opb			plb2opb_bridge	1.01.a
itagppc_0			itagppc_cntrl	2.00.a
iocm_cntrl			isbram_if_cntrl	3.00.a
docm_cntrl			dsbram_if_cntrl	3.00.a
LED_7SEGMENT			opb_gpio	3.01.b
LED_7SEGMENT_1			opb_gpio	3.01.b
LEDs_4Bit			opb_gpio	3.01.b
Push_Buttons_4bit			opb_gpio	3.01.b
DIPSWs_4Bit			opb_gpio	3.01.b
RS232			opb_uartlite	1.00.b
opb_intc_0			opb_intc	1.00.c
reset_block			proc_sys_reset	1.00.a
isocm_bram			bram_block	1.00.a
dsocm_bram			bram_block	1.00.a
dcm_0			dcm_module	1.00.a

Legend

Master
 Slave
 Master/Slave
 Target
 Initiator
 Connected
 Unconnected

[Platform Studi...] [System Assembly...]

(Console Log)

Output Warnings Errors