

Below is a summary of the system you have created. Please review the information below. If it is correct, hit <Generate> to enter the information into the XPS data base and generate the system files. Otherwise return to the previous page to make corrections.

Processor: PPC 405
 Processor clock frequency: 200.000000 MHz
 Bus clock frequency: 50.000000 MHz
 Debug interface: FPGA JTAG
 On Chip Memory : 320 KB

The address maps below have been automatically assigned. You can modify them using the editing features of XPS.

Processor OCM:

Core Name	Instance Name	Base Addr	High Addr
isbram_if_cntrlr	iocm_cntrlr	0xFFFE0000	0xFFFFFFFF

Processor OCM:

Core Name	Instance Name	Base Addr	High Addr
dsbram_if_cntrlr	docm_cntrlr	0x21800000	0x2180FFFF

PLB Bus : PLB_V34 Inst. name: plb Attached Components:

Core Name	Instance Name	Base Addr	High Addr
plb2opb_bridge	plb2opb_C_RNG0_BA:	0x40600000	0x4060FFFF
plb_bram_if_cntrlr	plb_bram_if_cntrlr_1	0x00000000	0x0001FFFF

OPB Bus : OPB_V20 Inst. name: opb Attached Components:

Core Name	Instance Name	Base Addr	High Addr
opb_uartlite	RS232	0x40600000	0x4060FFFF