

Category

- Programming Properties
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Programming Properties

General Programming Properties

Verify

General CPLD And PROM Properties

Erase Before Programming Read Protect

Prom/CoolRunner-II Usercode (8 Hex Digits)

CPLD Specific Properties

Write Protect Functional Test On-The-Fly Program

XPLA UES Enter up to 13 characters

PROM Specific Properties

Load FPGA Parallel Mode Use D4 for CF

Virtex-II/Virtex-4 Programming Properties

Pulse PROG Secure Mode Program Key