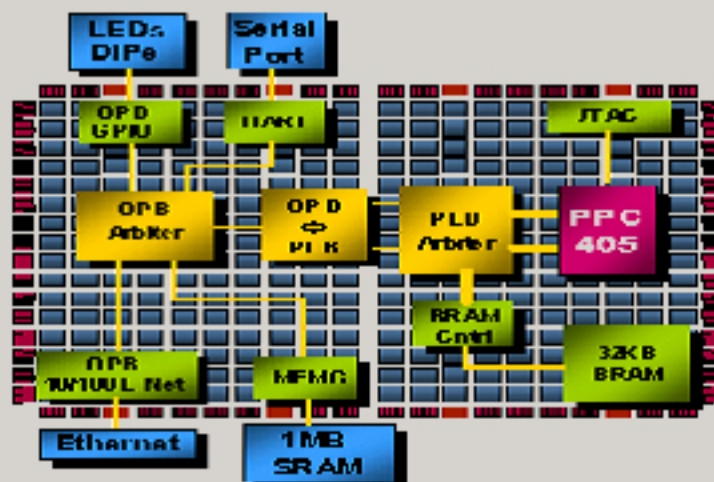


Architecture: Device: Package: Speed grade:  
virtex2p XC2VP70 FF1517 -6

-Processors

- ☐ MicroBlaze
- ☒ PowerPC



## - Processor description

The PowerPC 405 core is a 32-bit implementation of a RISC PowerPC embedded-environment architecture. It is integrated into the Virtex-II Pro and Virtex-4 FX device using the IP-Immersion technology and supported by CoreConnect bus infrastructure and extensive IP cores for peripherals and utilities.

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