














Pick the Xilinx Platform Studio project to import into...

Look in:  adderxxps    

-  __xps
-  data
-  etc
-  hdl
-  implementation
-  pcores
-  ppc405_0
-  ppc405_1
-  src
-  synthesis
-  TestAdd
-  TestApp_Memory
-  system.xmp

File name:

Open

Files of type:

Cancel

 EDK export settings

Export to ...

EDK Project location:



OK

Cancel